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1 diff -ruN f1c500s_uboot_old/config f1c500s_uboot_new/config
2 --- f1c500s_uboot_old/config 2018-12-23 18:04:40.00000000 +0800
3 +++ f1c500s_uboot_new/config 2018-12-31 23:14:48.478954005 +0800
4 @@ -199,8 +199,23 @@
5 # CONFIG_I2C2_ENABLE is not set
6 # CONFIG_R_I2C_ENABLE is not set
7 # CONFIG_AXP_GPIO is not set
8 -# CONFIG_VIDEO_SUNXI is not set
9 +CONFIG_VIDEO_SUNXI=y
10 +CONFIG_VIDEO_LCD_MODE=y
11 +CONFIG_VIDEO_LCD_DCLK_PHASE=1
12 +CONFIG_VIDEO_LCD_POWER=y
13 +CONFIG_VIDEO_LCD_RESET=y
14 +CONFIG_VIDEO_LCD_BL_EN=y
15 +CONFIG_VIDEO_LCD_BL_PWM=y
16 +CONFIG_VIDEO_LCD_BL_PWM_ACTIVE_LOW=y
17 +# CONFIG_VIDEO_LCD_PANEL_I2C is not set
18 +CONFIG_VIDEO_LCD_IF_PARALLEL=y
19 # CONFIG_SUNXI_DE2 is not set
20 +CONFIG_VIDEO_LCD_PANEL_PARALLEL=y
21 +# CONFIG_VIDEO_LCD_PANEL_LVDS is not set
22 +# CONFIG_VIDEO_LCD_PANEL_MIPI_4_LANE_513_MBPS_VIA_SSD2828 is not set
23 +# CONFIG_VIDEO_LCD_PANEL_EDP_4_LANE_1620M_VIA_AN9804 is not set
24 +# CONFIG_VIDEO_LCD_PANEL_HITACHI_T109M2VM is not set
25 +# CONFIG_VIDEO_LCD_T109M2VM is not set
26 CONFIG_SATAPIR=y
27 CONFIG_GMAC_TX_DELAY=0
28 CONFIG_SPL_STACK_R_ADDR=0x81e00000
29 @@ -518,6 +533,7 @@
30 #
31 # Misc commands
32 #
33 +# CONFIG_CMD_BMP is not set
34 +# CONFIG_CMD_ESP is not set
35 # CONFIG_CMD_BKOPS_ENABLE is not set
36 # CONFIG_CMD_CACHE is not set
37 @@ -620,11 +636,21 @@
38 # CONFIG_VIDEO_VESA is not set
39 # CONFIG_VIDEO_LCD_AN9804 is not set
40 # CONFIG_VIDEO_LCD_SSD2828 is not set
41 +# CONFIG_VIDEO_LCD_HITACHI_T109M2VM is not set
42 # CONFIG_VIDEO_MVEBU is not set
43 # CONFIG_DISPLAY is not set
44 +# CONFIG_VIDEO_SPL_DCU_FB is not set
45 # CONFIG_VIDEO_TEGRA20 is not set
46 # CONFIG_VIDEO_BRIDGE is not set
47 -# CONFIG_VIDEO is not set
48 +CONFIG_VIDEO=y
49 +CONFIG_CFB_CONSOLE=y
50 +# CONFIG_CFB_CONSOLE_ANSI is not set
51 +CONFIG_VGA_AS_SINGLE_DEVICE=y
52 +CONFIG_VIDEO_SW_CURSOR=y
53 +# CONFIG_CONSOLE_EXTRA_INFO is not set
54 +CONFIG_CONSOLE_SCROLL_LINES=1
55 +CONFIG_SYS_CONSOLE_BG_COL=0x00
56 +CONFIG_SYS_CONSOLE_FG_COL=0xa0
57 # CONFIG_LCD is not set
58 # CONFIG_VIDEO_SIMPLE is not set
59 # CONFIG_VIDEO_DT_SIMPLEFB is not set
60 diff -ruN f1c500s_uboot_old/config.old f1c500s_uboot_new/config.old
61 --- f1c500s_uboot_old/config.old 2018-12-18 21:17:14.00000000 +0800
62 +++ f1c500s_uboot_new/config.old 2018-12-31 22:54:10.936830939 +0800
63 @@ -265,11 +265,12 @@
64 # CONFIG_QSPI_BOOT is not set
65 # CONFIG_SATA_BOOT is not set
66 # CONFIG_SD_BOOT is not set
67 -# CONFIG_SPL_BOOT is not set
68 -CONFIG_BOOTDELAY=2
69 -# CONFIG_USE_BOOTARGS is not set
70 +CONFIG_SPL_BOOT=y
71 +CONFIG_BOOTDELAY=0
72 +CONFIG_USE_BOOTARGS=y
73 +CONFIG_BOOTARGS="console=ttyS1,115200 earlyprintk panic=5 rootwait root=/dev/mmcblk0p1 earlyprintk rw rootfstype=ext4"
74 +CONFIG_USE_BOOTCOMMAND=y
75 -CONFIG_BOOTCOMMAND="run distro bootcmd"
76 +CONFIG_BOOTCOMMAND="sf probe 0:0 50000000; sf read 0x80C00000 0x100000 0x1000; sf read 0x80008000 0x110000 0x100000; bootz 0x80008000 - 0x80C00000"
77 #
78 # Console
79 #
80 diff -ruN f1c500s_uboot_old/drivers/video/cfb_console.c f1c500s_uboot_new/drivers/video/cfb_console.c
81 --- f1c500s_uboot_old/drivers/video/cfb_console.c 2018-12-18 21:17:16.000000000 +0800
82 +++ f1c500s_uboot_new/drivers/video/cfb_console.c 2019-01-01 00:18:55.124189732 +0800
83 @@ -1719,6 +1719,8 @@
84 );
85 };
86
87 +extern void r61520_lcd_cmd(uint32_t val);
88 +extern void r61520_lcd_dat(uint32_t val);
89 static void plot_logo_or_black(void *screen, int x, int y, int black)
90 {
91     @@ -1771,6 +1773,7 @@
92     }
93     }
94     }
95     }
96     + r61520_lcd_cmd(0x2c);
97     while (ycount--) {
98     #if defined(VIDEO_FB_16BPP_PIXEL_SWAP)
99         int spos = x;
100     @@ -1786,6 +1789,7 @@
101         g = logo_green[*source - VIDEO_LOGO_LUT_OFFSET];
102         b = logo_blue[*source - VIDEO_LOGO_LUT_OFFSET];
103     }
104     + r61520_lcd_dat((((uint16_t)r & 0xf8) << 8) | (((uint16_t)g & 0xfc) << 3) | (((uint16_t)b & 0xf8) >> 3));
105     }
106     switch (VIDEO_DATA_FORMAT) {
107     case GDF_SBIT_INDEX:
108     @@ -2010,6 +2014,15 @@
109     memset(video_fb.address,
110            (VIDEO_VISIBLE_ROWS * VIDEO_LINE_LEN) / sizeof(int), bgx);
111     #endif
112     + uint16_t x, y;
113     +
114     + r61520_lcd_cmd(0x2c);
115     + for(y=0; y<240; y++){
116     +     for(x=0; x<320; x++){
117     +         r61520_lcd_dat(0);
118     +     }
119     + }
120     + }
121     }
122     }
123     static int cfg_video_init(void)
124     @@ -2102,9 +2115,10 @@
125
126     #ifdef CONFIG_VIDEO_LOGO
127     /* Plot the logo and get start point of console */
128     - debug("Video: Drawing the logo...\n");
129     + printf("Video: Drawing the logo...\n");
130     video_console_address = video_logo0;
131     #else
132     + printf("Video: assign fb address:\n");
133     video_console_address = video_fb.address;
134     #endif
135
136     diff -ruN f1c500s_uboot_old/drivers/video/Kconfig f1c500s_uboot_new/drivers/video/Kconfig
137     --- f1c500s_uboot_old/drivers/video/Kconfig 2018-12-18 21:17:16.000000000 +0800
138     +++ f1c500s_uboot_new/drivers/video/Kconfig 2018-12-31 23:14:16.462853497 +0800
139     @@ -660,4 +660,9 @@
140     @@ -660,4 +660,9 @@
141     The video output is initialized by U-Boot, and kept by the
142     kernel.
143     +config VIDEO_SUNXI
144     + bool "Enable framebuffer support for Miyoo handheld"
145     + help
146     + Enables GPIO-based framebuffer support for Miyoo handheld.
147     +
148     endmenu
149     diff -ruN f1c500s_uboot_old/drivers/video/sunxi_display.c f1c500s_uboot_new/drivers/video/sunxi/sunxi_display.c
150     --- f1c500s_uboot_old/drivers/video/sunxi_display.c 2018-12-22 15:49:02.000000000 +0800
151     +++ f1c500s_uboot_new/drivers/video/sunxi/sunxi_display.c 2019-01-01 00:04:19.780553450 +0800
152     @@ -68,6 +68,236 @@
153     { 720, 480, 60, 37037, 27000, 116, 20, 16, 27, 2, 2, 0, FB_VMODE_INTERLACED },
154     };
155
156     +static void sunxi_ledc_output(uint32_t is_data, uint32_t val)
157     +{
158     +     uint32_t ret;
159     +     struct sunxi_gpio_reg * const gpio = (struct sunxi_gpio_reg *)SUNXI_P10_BASE;
160     +
161     +     ret = (val & 0x00ff) << 1;
162     +     ret |= (val & 0xf00) << 2;
163     +     ret |= is_data ? 0x8000 : 0;
164     +     ret |= 0x100000;
165     +     writel(ret, &gpio->gpio_bank[SUNXI_GPIO_D].dat);
166     +     ret = 0x40000;
167     +     writel(ret, &gpio->gpio_bank[SUNXI_GPIO_D].dat);
168     + }
169     +
170     +void r61520_lcd_cmd(uint32_t val)
171     +{
172     +     sunxi_ledc_output(0, val);
173     + }
174     +
175     +void r61520_lcd_dat(uint32_t val)

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176 +
177 + sunxi_ledc_output(1, val);
178 +
179 +
180 +static void sunxi_ledc_gpio_config(void)
181 +{
182 + struct sunxi_gpio_reg * const gpio = (struct sunxi_gpio_reg *)SUNXI_P10_BASE;
183 +
184 + writel(0x11111117, &gpio->gpio_bank[SUNXI_GPIO_D].cfg[0]); // 0x11111117
185 + writel(0x11111171, &gpio->gpio_bank[SUNXI_GPIO_D].cfg[1]); // 0x11111171
186 + writel(0x00111111, &gpio->gpio_bank[SUNXI_GPIO_D].cfg[2]); // 0x00111111
187 + writel(0xffffffff, &gpio->gpio_bank[SUNXI_GPIO_D].dat); // CS/RD/RS/WR
188 +
189 +
190 +static void r61520_lcd_init(void)
191 +{
192 + uint32_t ret;
193 + struct sunxi_gpio_reg * const gpio = (struct sunxi_gpio_reg *)SUNXI_P10_BASE;
194 +
195 + ret = readl(&gpio->gpio_bank[SUNXI_GPIO_E].cfg[0]);
196 + ret&= 0x0fffff;
197 + writel(ret, &gpio->gpio_bank[SUNXI_GPIO_E].cfg[0]);
198 + ret = readl(&gpio->gpio_bank[SUNXI_GPIO_E].dat);
199 + ret = 0x0000;
200 + writel(ret, &gpio->gpio_bank[SUNXI_GPIO_E].dat);
201 +
202 + ret = readl(&gpio->gpio_bank[SUNXI_GPIO_E].cfg[1]);
203 + ret&= 0xfffffff;
204 + writel(ret, &gpio->gpio_bank[SUNXI_GPIO_E].cfg[1]);
205 + ret = readl(&gpio->gpio_bank[SUNXI_GPIO_E].dat);
206 + ret&= 0x0800;
207 + writel(ret, &gpio->gpio_bank[SUNXI_GPIO_E].dat);
208 +
209 + mdelay(150);
210 + ret = 0x0800;
211 + writel(ret, &gpio->gpio_bank[SUNXI_GPIO_E].dat);
212 + mdelay(150);
213 +
214 + r61520_lcd_cmd(0xb0);
215 + r61520_lcd_dat(0x00);
216 +
217 + r61520_lcd_cmd(0xb1);
218 + r61520_lcd_dat(0x00);
219 +
220 + r61520_lcd_cmd(0xb3);
221 + r61520_lcd_dat(0x02);
222 + r61520_lcd_dat(0x00);
223 + r61520_lcd_dat(0x00);
224 + r61520_lcd_dat(0x00);
225 + r61520_lcd_dat(0x00);
226 +
227 + r61520_lcd_cmd(0xb4);
228 + r61520_lcd_dat(0x00);
229 +
230 + r61520_lcd_cmd(0xc0);
231 + r61520_lcd_dat(0x07);
232 + r61520_lcd_dat(0x4f);
233 + r61520_lcd_dat(0x00);
234 + r61520_lcd_dat(0x00);
235 + r61520_lcd_dat(0x00);
236 + r61520_lcd_dat(0x00);
237 + r61520_lcd_dat(0x01);
238 + r61520_lcd_dat(0x33);
239 +
240 + r61520_lcd_cmd(0xc1);
241 + r61520_lcd_dat(0x01);
242 + r61520_lcd_dat(0x00);
243 + r61520_lcd_dat(0x1a);
244 + r61520_lcd_dat(0x08);
245 + r61520_lcd_dat(0x08);
246 +
247 + r61520_lcd_cmd(0xc3);
248 + r61520_lcd_dat(0x01);
249 + r61520_lcd_dat(0x00);
250 + r61520_lcd_dat(0x1a);
251 + r61520_lcd_dat(0x08);
252 + r61520_lcd_dat(0x08);
253 +
254 + r61520_lcd_cmd(0xc4);
255 + r61520_lcd_dat(0x11);
256 + r61520_lcd_dat(0x01);
257 + r61520_lcd_dat(0x43);
258 + r61520_lcd_dat(0x01);
259 +
260 + r61520_lcd_cmd(0xc8);
261 + r61520_lcd_dat(0x00);
262 + r61520_lcd_dat(0x0a);
263 + r61520_lcd_dat(0x08);
264 + r61520_lcd_dat(0x8a);
265 + r61520_lcd_dat(0x08);
266 + r61520_lcd_dat(0x09);
267 + r61520_lcd_dat(0x09);
268 + r61520_lcd_dat(0x10);
269 + r61520_lcd_dat(0x00);
270 + r61520_lcd_dat(0x23);
271 + r61520_lcd_dat(0x10);
272 + r61520_lcd_dat(0x05);
273 + r61520_lcd_dat(0x05);
274 + r61520_lcd_dat(0x0a);
275 + r61520_lcd_dat(0x0a);
276 + r61520_lcd_dat(0x08);
277 + r61520_lcd_dat(0x05);
278 + r61520_lcd_dat(0x00);
279 + r61520_lcd_dat(0x10);
280 + r61520_lcd_dat(0x00);
281 +
282 + r61520_lcd_cmd(0xc9);
283 + r61520_lcd_dat(0x00);
284 + r61520_lcd_dat(0x0a);
285 + r61520_lcd_dat(0x08);
286 + r61520_lcd_dat(0x8a);
287 + r61520_lcd_dat(0x08);
288 + r61520_lcd_dat(0x09);
289 + r61520_lcd_dat(0x05);
290 + r61520_lcd_dat(0x10);
291 + r61520_lcd_dat(0x00);
292 + r61520_lcd_dat(0x23);
293 + r61520_lcd_dat(0x10);
294 + r61520_lcd_dat(0x05);
295 + r61520_lcd_dat(0x09);
296 + r61520_lcd_dat(0x88);
297 + r61520_lcd_dat(0x0a);
298 + r61520_lcd_dat(0x08);
299 + r61520_lcd_dat(0x0a);
300 + r61520_lcd_dat(0x00);
301 + r61520_lcd_dat(0x23);
302 + r61520_lcd_dat(0x00);
303 +
304 + r61520_lcd_cmd(0xca);
305 + r61520_lcd_dat(0x00);
306 + r61520_lcd_dat(0x0a);
307 + r61520_lcd_dat(0x08);
308 + r61520_lcd_dat(0x8a);
309 + r61520_lcd_dat(0x08);
310 + r61520_lcd_dat(0x09);
311 + r61520_lcd_dat(0x05);
312 + r61520_lcd_dat(0x10);
313 + r61520_lcd_dat(0x00);
314 + r61520_lcd_dat(0x23);
315 + r61520_lcd_dat(0x10);
316 + r61520_lcd_dat(0x05);
317 + r61520_lcd_dat(0x09);
318 + r61520_lcd_dat(0x88);
319 + r61520_lcd_dat(0x0a);
320 + r61520_lcd_dat(0x08);
321 + r61520_lcd_dat(0x0a);
322 + r61520_lcd_dat(0x00);
323 + r61520_lcd_dat(0x23);
324 + r61520_lcd_dat(0x00);
325 +
326 + r61520_lcd_cmd(0xd0);
327 + r61520_lcd_dat(0x07);
328 + r61520_lcd_dat(0xc6);
329 + r61520_lcd_dat(0xdc);
330 +
331 + r61520_lcd_cmd(0xd1);
332 + r61520_lcd_dat(0x75);
333 + r61520_lcd_dat(0x0a);
334 + r61520_lcd_dat(0x02);
335 +
336 + r61520_lcd_cmd(0xd2);
337 + r61520_lcd_dat(0x63);
338 + r61520_lcd_dat(0x24);
339 +
340 + r61520_lcd_cmd(0xd4);
341 + r61520_lcd_dat(0x63);
342 + r61520_lcd_dat(0x24);
343 +
344 + r61520_lcd_cmd(0xd8);
345 + r61520_lcd_dat(0x07);
346 + r61520_lcd_dat(0x07);
347 +
348 + r61520_lcd_cmd(0xe0);
349 + r61520_lcd_dat(0x00);
350 + r61520_lcd_dat(0x00);
351 +
352 + r61520_lcd_cmd(0x13);
353 +
354 + r61520_lcd_cmd(0x20);
355 +
356 + r61520_lcd_cmd(0x35);
357 + r61520_lcd_dat(0x00);
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358 +
359 + r61520_lcd_cad(0x44);
360 + r61520_lcd_dat(0x00);
361 + r61520_lcd_dat(0x30);
362 +
363 + r61520_lcd_cad(0x36);
364 + r61520_lcd_dat(0xe0);
365 +
366 + r61520_lcd_cad(0x3a);
367 + r61520_lcd_dat(0x55);
368 +
369 + r61520_lcd_cad(0x2a);
370 + r61520_lcd_dat(0x00);
371 + r61520_lcd_dat(0x00);
372 + r61520_lcd_dat(0x00);
373 + r61520_lcd_dat(0x3f);
374 +
375 + r61520_lcd_cad(0x2b);
376 + r61520_lcd_dat(0x00);
377 + r61520_lcd_dat(0x00);
378 + r61520_lcd_dat(0x00);
379 + r61520_lcd_dat(0xe7);
380 +
381 + r61520_lcd_cad(0x11);
382 + r61520_lcd_cad(0x29);
383 + r61520_lcd_cad(0x2c);
384 +)
385 +
386 +#ifdef CONFIG_VIDEO_HDMI
387 +
388 +/s
389 @@ -1130,7 +1360,8 @@
390
391     switch (sunxi_display_monitor) {
392     case sunxi_monitor_none:
393         return NULL;
394     + break; //return NULL;
395     + case sunxi_monitor_dvi:
396     + case sunxi_monitor_hdmi:
397     +     if (!sunxi_has_hdmi()) {
398     +@@ -1226,6 +1457,8 @@
399     +         graphic_device->winSizeY = mode->yres - 2 * overscan.y;
400     +         graphic_device->pinSizeX = mode->xres * graphic_device->gdfBytesPP;
401     +
402     + sunxi_ledc_gpio_config();
403     + r61520_lcd_init();
404     + return graphic_device;
405     + }
406
407
408 diff --run f1c500s_uboot_old/include/autoconf.mk f1c500s_uboot_new/include/autoconf.mk
409 --- f1c500s_uboot_old/include/autoconf.mk 2018-12-23 18:04:47.000000000 +0800
410 +++ f1c500s_uboot_new/include/autoconf.mk 2018-12-31 23:42:42.344680283 +0800
411 @@ -1,4 +1,5 @@
412 CONFIG_SYS_SPL_U_BOOT_OFFS=0x8000
413 +CONFIG_VIDEO_BMP_LOGO=y
414 CONFIG_BOOTM_NETBSD=y
415 CONFIG_BOOTM_VXWORKS=y
416 CONFIG_MMC_SUNXI_SLOT=0
417 @@ -12,6 +13,7 @@
418 CONFIG_SYS_MONITOR_LEN="(768 << 10)"
419 CONFIG_BOOTM_LINUX=y
420 CONFIG_SYS_SPD_BUS_NUM=0
421 +CONFIG_VIDEO_STD_TIMINGS=y
422 CONFIG_MISC_INIT_R=y
423 CONFIG_INITRD_16=y
424 CONFIG_SUPPORT_RAW_INITRD=y
425 @@ -33,9 +35,12 @@
426 CONFIG_SYS_LONGHELP=y
427 CONFIG_SPL_STACK="LOW_LEVEL_SRAM_STACK"
428 CONFIG_SYS_MAX_FLASH_SECT=512
429 +CONFIG_VIDEO_LOGO=y
430 +CONFIG_SUNXI_MAX_FB_SIZE="(16 << 20)"
431 CONFIG_STANDALONE_LOAD_ADDR=$(CONFIG_SYS_LOAD_ADDR)
432 CONFIG_ZLIB=y
433 CONFIG_LIB_UUID=y
434 +CONFIG_I2C_EEID=y
435 CONFIG_AUTO_COMPLETE=y
436 CONFIG_SYS_316550_CLK=100000000
437 CONFIG_GZIP=y
438 diff --run f1c500s_uboot_old/include/config/auto.conf f1c500s_uboot_new/include/config/auto.conf
439 --- f1c500s_uboot_old/include/config/auto.conf 2018-12-23 18:04:47.000000000 +0800
440 +++ f1c500s_uboot_new/include/config/auto.conf 2018-12-31 23:42:42.352680283 +0800
441 @@ -51,6 +51,7 @@
442 CONFIG_PRE_CONSOLE_BUFFER=y
443 CONFIG_USE_STORAGE=y
444 CONFIG_GMAC_TX_DELAY=0
445 +CONFIG_VIDEO_SW_CURSOR=y
446 CONFIG_CMD_BOOTEFI_HELLO_COMPILE=y
447 CONFIG_EFI_LOADER=y
448 CONFIG_CMD_PING=y
449 @@ -63,6 +64,7 @@
450 CONFIG_SYS_STDIO_DEREGISTER=y
451 CONFIG_SYS_CPU="arm926ejs"
452 CONFIG_NETDEVICES=y
453 +CONFIG_VIDEO_LCD_BL_PWM_ACTIVE_LOW=y
454 CONFIG_USB_GADGET_PRODUCT_NUM=0x1010
455 CONFIG_SPL_MMC_SUPPORT=y
456 CONFIG_CC_OPTIMIZE_FOR_SIZE=y
457 @@ -65,6 +67,7 @@
458 CONFIG_TPL_SYS_MALLOC_F_LEN=0x400
459 CONFIG_CMD_SETEXPR=y
460 CONFIG_CMD_SF=y
461 +CONFIG_VIDEO=y
462 CONFIG_DM_DEVICE_REMOVE=y
463 CONFIG_SYS_CGCHLDR_SIZE=32
464 CONFIG_CMD_EXPRES=y
465 @@ -108,6 +111,7 @@
466 CONFIG_SPL_BOOT=y
467 CONFIG_ENV_IS_IN_SPL_FLASH=y
468 CONFIG_SYS_BOARD="sunxi"
469 +CONFIG_CFB_CONSOLE=y
470 CONFIG_SYS_CONFIG_NAME="suniv"
471 CONFIG_SPL_SUNXI=y
472 CONFIG_CMD_NFS=y
473 @@ -116,7 +120,9 @@
474 CONFIG_SPL_FLASH=y
475 CONFIG_USE_ARCH_MEMSET=y
476 CONFIG_NET=y
477 +CONFIG_VIDEO_LCD_RESET="--"
478 CONFIG_DM_WARN=y
479 +CONFIG_VIDEO_LCD_PANEL_PARALLEL=y
480 CONFIG_MCPWM="--"
481 CONFIG_USBO_VBUS_PIN="--"
482 CONFIG_PARTITION_UUIDS=y
483 @@ -130,12 +136,16 @@
484 CONFIG_CMD_FS_GENERIC=y
485 CONFIG_SUPPORT_OF_CONTROL=y
486 CONFIG_SPL_BOOTSTAGE_RECORD_COUNT=5
487 +CONFIG_SYS_CONSOLE_FG_COL=0xa0
488 CONFIG_DISTRO_DEFAULTS=y
489 CONFIG_CMD_EXT2=y
490 CONFIG_DM_STDIU=y
491 CONFIG_CPU_ARMV8EJS=y
492 +CONFIG_VIDEO_LCD_BL_PWM="--"
493 +CONFIG_CONSOLE_SCROLL_LINES=1
494 CONFIG_LOCALVERSION="--"
495 CONFIG_MMC3_PINS="--"
496 +CONFIG_VIDEO_SUNXI=y
497 CONFIG_SUPPORT_SPL=y
498 CONFIG_CMD_LIN=y
499 CONFIG_DTOC=y
500 @@ -155,6 +165,7 @@
501 CONFIG_INITIAL_USB_SCAN_DELAY=0
502 CONFIG_LOCALVERSION_AUTO=y
503 CONFIG_DRAW_CLK=156
504 +CONFIG_SYS_CONSOLE_FG_COL=0x00
505 CONFIG_SYS_MMCSD_RAW_MODE_U_BOOT_USE_SECTOR=y
506 CONFIG_SPL_SYS_MALLOC_F_LEN=0x400
507 CONFIG_SYS_ARCH="arm"
508 @@ -167,9 +175,11 @@
509 CONFIG_BAUDRATE=115200
510 CONFIG_SYS_MMCSD_RAW_MODE_U_BOOT_SECTOR=0x50
511 CONFIG_FAT_WRITE=y
512 +CONFIG_VGA_AS_SINGLE_DEVICE=y
513 CONFIG_PARTITIONS=y
514 CONFIG_CMD_TST=y
515 CONFIG_DISPLAY_CPUINFO=y
516 +CONFIG_VIDEO_LCD_IF_PARALLEL=y
517 CONFIG_BOOTP_VCI_STRING="U-Boot.arm"
518 CONFIG_DM_SPL_FLASH=y
519 CONFIG_CMD_GPIO=y
520 @@ -183,15 +196,18 @@
521 CONFIG_CMD_PPT=y
522 CONFIG_SYS_USB_EVENT_POLL=y
523 CONFIG_CMD_FAT=y
524 +CONFIG_VIDEO_LCD_POWER="--"
525 CONFIG_SPL_FLASH_MICRONLY=y
526 CONFIG_MKIMAGE_DTC_PATH="dtc"
527 CONFIG_SPL_POWER_SUPPORT=y
528 CONFIG_PILDEP=y
529 CONFIG_MMC_SUNXI_SLOT_EXTRA=1
530 +CONFIG_VIDEO_LCD_BL_EN="--"
531 CONFIG_EFI_PARTITION_ENTRIES_OFF=0
532 CONFIG_CMD_NET=y
533 CONFIG_SPL_RAW_IMAGE_SUPPORT=y
534 CONFIG_MENU=y
535 +CONFIG_VIDEO_LCD_MODE="--"
536 CONFIG_CONS_INDEX=2
537 CONFIG_DEFAULT_DEVICE_TREE="suniv-f1c500s-miyoo"
538 CONFIG_NET_TFTP_VARS=y
539 @@ -207,6 +223,7 @@
```

```
540 CONFIG_SYS_MALLOC_F=y
541 CONFIG_SPL_USE_ARCH_MEMCPY=y
542 CONFIG_CMD_IMPORTENV=y
543 #CONFIG_VIDEO_LCD_DCLK_PHASE=1
544 CONFIG_DRAM_ZQ=0
545 CONFIG_MMC3_CD_PIN=""
546 CONFIG_SYS_MALLOC_F_LEN=0x400
547 diff -rux f1c500s_uboot_old/include/config/uboot.release f1c500s_uboot_new/include/config/uboot.release
548 --- f1c500s_uboot_old/include/config/uboot.release 2018-12-23 18:04:48.00000000 +0800
549 +++ f1c500s_uboot_new/include/config/uboot.release 2018-12-31 22:48:23.041124799 +0800
550 @@ -1,4 @@
551 -2018, 01 "" #b74f594-dirty
552 +2018, 01 ""
553 diff -rux f1c500s_uboot_old/include/configs/sunxi-common.h f1c500s_uboot_new/include/configs/sunxi-common.h
554 --- f1c500s_uboot_old/include/configs/sunxi-common.h 2018-12-18 21:17:16.00000000 +0800
555 +++ f1c500s_uboot_new/include/configs/sunxi-common.h 2018-12-31 23:41:03.512654797 +0800
556 @@ -297,6 +297,7 @@
557 #define CONFIG_SUNXI_MAX_FB_SIZE (16 << 20)
558
559 #define CONFIG_VIDEO_LOGO
560 #define CONFIG_VIDEO_BMP_LAGO
561 #define CONFIG_VIDEO_STD_TIMINGS
562 #define CONFIG_I2C_EDID
563 #define VIDEO_LINE_LEN (gd->plnSizeX)
564 diff -rux f1c500s_uboot_old/include/generated/autoconf.h f1c500s_uboot_new/include/generated/autoconf.h
565 --- f1c500s_uboot_old/include/generated/autoconf.h 2018-12-23 18:04:47.00000000 +0800
566 +++ f1c500s_uboot_new/include/generated/autoconf.h 2018-12-31 23:42:41.896680098 +0800
567 @@ -53,6 +53,7 @@
568 #define CONFIG_PRE_CONSOLE_BUFFER 1
569 #define CONFIG_USB_STORAGE 1
570 #define CONFIG_GMAC_TX_DELAY 0
571 #define CONFIG_VIDEO_SX_CURSOR 1
572 #define CONFIG_CMD_BOOTEFI_HELLO_COMPILE 1
573 #define CONFIG_EFI_LOADER 1
574 #define CONFIG_CMD_PING 1
575 @@ -65,6 +66,7 @@
576 #define CONFIG_SYS_STDIQ_DEREGISTER 1
577 #define CONFIG_SYS_CPI_ARM926EJS
578 #define CONFIG_NEUTRYCES 1
579 #define CONFIG_VIDEO_LCD_BL_PWM_ACTIVE_LOW 1
580 #define CONFIG_USB_GADGET_PRODUCT_NUM 0x1010
581 #define CONFIG_SPL_MMC_SUPPORT 1
582 #define CONFIG_CC_OPTIMIZE_FOR_SIZE 1
583 @@ -87,6 +89,7 @@
584 #define CONFIG_TPL_SYS_MALLOC_F_LEN 0x400
585 #define CONFIG_CMD_SETEXPR 1
586 #define CONFIG_CMD_SF 1
587 #define CONFIG_VIDEO 1
588 #define CONFIG_DM_DEVICE_REMOVE 1
589 #define CONFIG_SYS_CACHELINE_SIZE 32
590 #define CONFIG_CMD_EXPORTENV 1
591 @@ -110,6 +113,7 @@
592 #define CONFIG_SPL_BOOT 1
593 #define CONFIG_ENV_IS_IN_SPL_FLASH 1
594 #define CONFIG_SYS_BOARD "sunxi"
595 #define CONFIG_CBS_CONSOLE 1
596 #define CONFIG_SYS_CONFIG_NAME "suniv"
597 #define CONFIG_SPL_SUNXI 1
598 #define CONFIG_CMD_NFS 1
599 @@ -118,7 +122,9 @@
600 #define CONFIG_SPL_FLASH 1
601 #define CONFIG_USE_ARCH_MEMSET 1
602 #define CONFIG_NET 1
603 #define CONFIG_VIDEO_LCD_RESET ""
604 #define CONFIG_DM_WARM 1
605 #define CONFIG_VIDEO_LCD_PANEL_PARALLEL 1
606 #define CONFIG_MACPWR ""
607 #define CONFIG_USB_VBUS_PIN ""
608 #define CONFIG_PARTITION_UUIDS 1
609 @@ -132,12 +138,16 @@
610 #define CONFIG_CMD_FS_GENERIC 1
611 #define CONFIG_SUPPORT_OF_CONTROL 1
612 #define CONFIG_SPL_BOOTSTAGE_RECORD_COUNT 5
613 #define CONFIG_SYS_CONSOLE_FG_COL 0xa0
614 #define CONFIG_DISTRO_DEFAULTS 1
615 #define CONFIG_CMD_EXT2 1
616 #define CONFIG_DM_STDIQ 1
617 #define CONFIG_CPU_ARM926EJS 1
618 #define CONFIG_VIDEO_LCD_BL_PWM ""
619 #define CONFIG_CONSOLE_SCROLL_LINES 1
620 #define CONFIG_LOCALVERSION ""
621 #define CONFIG_MMC3_PINS ""
622 #define CONFIG_VIDEO_SUNXI 1
623 #define CONFIG_SUPPORT_SPL 1
624 #define CONFIG_CMDLINE 1
625 #define CONFIG_DTOC 1
626 @@ -157,6 +167,7 @@
627 #define CONFIG_INITIAL_USB_SCAN_DELAY 0
628 #define CONFIG_LOCALVERSION_AUTO 1
629 #define CONFIG_DRAM_L16
630 #define CONFIG_SYS_CONSOLE_BG_COL 0x00
631 #define CONFIG_SYS_MMCSD_RAW_MODE_U_BOOT_USE_SECTOR 1
632 #define CONFIG_SPL_SYS_MALLOC_F_LEN 0x400
633 #define CONFIG_SYS_ARCH "arm"
634 @@ -169,9 +180,11 @@
635 #define CONFIG_BAUDRATE 115200
636 #define CONFIG_SYS_MMCSD_RAW_MODE_U_BOOT_SECTOR 0x50
637 #define CONFIG_FAT_WRITE 1
638 #define CONFIG_VGA_AS_SINGLE_DEVICE 1
639 #define CONFIG_PARTITIONS 1
640 #define CONFIG_CMD_ITEST 1
641 #define CONFIG_DISPLAY_CPUINFO 1
642 #define CONFIG_VIDEO_LCD_IF_PARALLEL 1
643 #define CONFIG_BOOTP_VCI_STRING "U-Boot.arm"
644 #define CONFIG_DM_SPL_FLASH 1
645 #define CONFIG_CMD_GPIO 1
646 @@ -185,15 +198,18 @@
647 #define CONFIG_CMD_FDT 1
648 #define CONFIG_SYS_USB_EVENT_POLL 1
649 #define CONFIG_CMD_FIT 1
650 #define CONFIG_VIDEO_LCD_POWER ""
651 #define CONFIG_SPL_FLASH_MACRONIX 1
652 #define CONFIG_MKIMAGE_DTC_PATH "dtc"
653 #define CONFIG_SPL_POWER_SUPPORT 1
654 #define CONFIG_PHYLIBFDT 1
655 #define CONFIG_MMC_SUNXI_SLOT_EXTRA -1
656 #define CONFIG_VIDEO_LCD_BL_EN
657 #define CONFIG_EFI_PARTITION_ENTRIES_OFF 0
658 #define CONFIG_CMD_NET 1
659 #define CONFIG_SPL_KM_IMAGE_SUPPORT 1
660 #define CONFIG_MENU 1
661 #define CONFIG_VIDEO_LCD_MODE ""
662 #define CONFIG_CONS_INDEX 2
663 #define CONFIG_DEFAULT_DEVICE_TREE "suniv-f1c500s-miyoo"
664 #define CONFIG_NET_TFTP_VARS 1
665 @@ -209,6 +225,7 @@
666 #define CONFIG_SYS_MALLOC_F 1
667 #define CONFIG_SPL_USE_ARCH_MEMCPY 1
668 #define CONFIG_CMD_IMPORTENV 1
669 #define CONFIG_VIDEO_LCD_DCLK_PHASE 1
670 #define CONFIG_DRAM_ZQ 0
671 #define CONFIG_MMC3_CD_PIN ""
672 #define CONFIG_SYS_MALLOC_F_LEN 0x400
673 diff -rux f1c500s_uboot_old/include/generated/generic-asm-offsets.h f1c500s_uboot_new/include/generated/generic-asm-offsets.h
674 --- f1c500s_uboot_old/include/generated/generic-asm-offsets.h 2018-12-18 21:17:16.00000000 +0800
675 +++ f1c500s_uboot_new/include/generated/generic-asm-offsets.h 2018-12-31 23:14:57.286981815 +0800
676 @@ -10,10 +10,10 @@
677 #define GENERATED_BD_INFO_SIZE 80 /* sizeof(struct bd_info) + 15) & ~15 0 */
678 #define GD_SIZE 200 /* sizeof(struct global_data) 0 */
679 #define GD_RD 0 /* offsetof(struct global_data, bd) 0 */
680 #define GD_MALLOC_BASE 148 /* offsetof(struct global_data, malloc_base) 0 */
681 #define GD_RELOCADDR 48 /* offsetof(struct global_data, relocaddr) 0 */
682 #define GD_RELOC_OFF 68 /* offsetof(struct global_data, reloc_off) 0 */
683 #define GD_START_ADDR_SF 64 /* offsetof(struct global_data, start_addr_sp) 0 */
684 #define GD_NEW_GD 72 /* offsetof(struct global_data, new_gd) 0 */
685 #define GD_MALLOC_BASE 152 /* offsetof(struct global_data, malloc_base) 0 */
686 #define GD_RELOCADDR 52 /* offsetof(struct global_data, relocaddr) 0 */
687 #define GD_RELOC_OFF 72 /* offsetof(struct global_data, reloc_off) 0 */
688 #define GD_START_ADDR_SF 68 /* offsetof(struct global_data, start_addr_sp) 0 */
689 #define GD_NEW_GD 76 /* offsetof(struct global_data, new_gd) 0 */
690
691 #endif
692 diff -rux f1c500s_uboot_old/include/generated/timestamp_autogenerated.h f1c500s_uboot_new/include/generated/timestamp_autogenerated.h
693 --- f1c500s_uboot_old/include/generated/timestamp_autogenerated.h 2018-12-23 18:04:47.00000000 +0800
694 +++ f1c500s_uboot_new/include/generated/timestamp_autogenerated.h 2019-01-01 09:52:55.002527413 +0800
695 @@ -1,5 +1,5 @@
696 #define U_BOOT_DATE "Dec 23 2018"
697 #define U_BOOT_TIME "18:04:47"
698 #define U_BOOT_DATE "Jan 01 2019"
699 #define U_BOOT_TIME "09:52:55"
700 #define U_BOOT_TZ "+0800"
701 #define U_BOOT_DMI_DATE "12/23/2018"
702 #define U_BOOT_BUILD_DATE 0x20181223
703 #define U_BOOT_DMI_DATE "01/01/2019"
704 #define U_BOOT_BUILD_DATE 0x20190101
705 diff -rux f1c500s_uboot_old/include/generated/version_autogenerated.h f1c500s_uboot_new/include/generated/version_autogenerated.h
706 --- f1c500s_uboot_old/include/generated/version_autogenerated.h 2018-12-23 18:04:48.00000000 +0800
707 +++ f1c500s_uboot_new/include/generated/version_autogenerated.h 2018-12-31 22:48:23.041124799 +0800
708 @@ -1,4 +1,4 @@
709 #define PLAIN_VERSION "2018.01-#b74f594-dirty"
710 #define PLAIN_VERSION "2018.01"
711 #define U_BOOT_VERSION "U-Boot " PLAIN_VERSION
712 #define CC_VERSION_STRING "arm-linux-gnueabi-gcc (Debian 6.3.0-18) 6.3.0 20170516"
713 #define LD_VERSION_STRING "GNU ld (GNU Binutils for Debian) 2.28"
714 diff -rux f1c500s_uboot_old/logo.sh f1c500s_uboot_new/logo.sh
715 --- f1c500s_uboot_old/logo.sh 1970-01-01 08:00:00.000000000 +0800
716 +++ f1c500s_uboot_new/logo.sh 2019-01-01 09:21:24.381580630 +0800
717 @@ -0,0 +1,3 @@
718 #!/bin/sh
719 # logo.sh xxx.jpg ./tools/logos/xxx.bmp
720 # destination $1 | cpmmount $1 | pmountbwp -bwp 8 > $2
721 Binary files f1c500s_uboot_old/scripts/kconfig/mconf and f1c500s_uboot_new/scripts/kconfig/mconf differ
```

```
722 diff -run f1c500s_uboot_old/spl/include/autoconf.mk f1c500s_uboot_new/spl/include/autoconf.mk
723 --- f1c500s_uboot_old/spl/include/autoconf.mk 2018-12-23 18:04:47.000000000 +0800
724 +++ f1c500s_uboot_new/spl/include/autoconf.mk 2018-12-31 23:42:42.340680280 +0800
725 @@ -1,4 +1,5 @@
726 CONFIG_SYS_SPI_U_BOOT_OFFS=0x8000
727 +CONFIG_VIDEO_BMP_LOGO=y
728 CONFIG_BOOTM_NETBSD=y
729 CONFIG_BOOTM_VXWORKS=y
730 CONFIG_MMC_SUNXI_SLOT=0
731 @@ -12,6 +13,7 @@
732 CONFIG_SYS_MONITOR_LEN="(768 << 10)"
733 CONFIG_BOOTM_LINUX=y
734 CONFIG_SYS_SPD_BUS_NUM=0
735 +CONFIG_VIDEO_STD_TIMINGS=y
736 CONFIG_MISC_INIT_F=y
737 CONFIG_INITRD_TAG=y
738 CONFIG_SPL_BUILD=y
739 @@ -29,9 +31,12 @@
740 CONFIG_BOARDDIR="board/sunxi"
741 CONFIG_SPL_STACK="LOW_LEVEL_SRAM_STACK"
742 CONFIG_SYS_MAX_FLASH_SECT=512
743 +CONFIG_VIDEO_LOAD=y
744 +CONFIG_SUNXI_MAX_FB_SIZE="(16 << 20)"
745 CONFIG_STANDALONE_LOAD_ADDR=$(CONFIG_SYS_LOAD_ADDR)
746 CONFIG_ZLIB=y
747 CONFIG_LB_HUID=y
748 +CONFIG_I2C_EUID=y
749 CONFIG_SYS_SSI6550_CLK=100000000
750 CONFIG_GZIP=y
751 CONFIG_SYS_INIT_RAM_SIZE=0x8000
752 Binary files f1c500s_uboot_old/tools/logos/miyoo_1.bmp and f1c500s_uboot_new/tools/logos/miyoo_1.bmp differ
753 Binary files f1c500s_uboot_old/tools/logos/miyoo_2.bmp and f1c500s_uboot_new/tools/logos/miyoo_2.bmp differ
754 Binary files f1c500s_uboot_old/tools/logos/miyoo_3.bmp and f1c500s_uboot_new/tools/logos/miyoo_3.bmp differ
755 diff -run f1c500s_uboot_old/tools/Makefile f1c500s_uboot_new/tools/Makefile
756 --- f1c500s_uboot_old/tools/Makefile 2018-12-18 21:17:16.000000000 +0800
757 +++ f1c500s_uboot_new/tools/Makefile 2019-01-01 09:52:27.086877753 +0800
758 @@ -220,7 +220,10 @@
759
760 # Generic logo
761 ifeq ($(LOGO_BMP),)
762 -LOGO_BMP= $(srctree)/$(src)/logos/denix.bmp
763 +LOGO_BMP= $(srctree)/$(src)/logos/miyoo_1.bmp
764 +LOGO_BMP= $(srctree)/$(src)/logos/miyoo_2.bmp
765 +LOGO_BMP= $(srctree)/$(src)/logos/miyoo_3.bmp
766 +LOGO_BMP= $(srctree)/$(src)/logos/denix.bmp
767
768 # Use board logo and fallback to vendor
769 ifneq ($(wildcard $(srctree)/$(src)/logos/$(BOARD).bmp),)
```



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