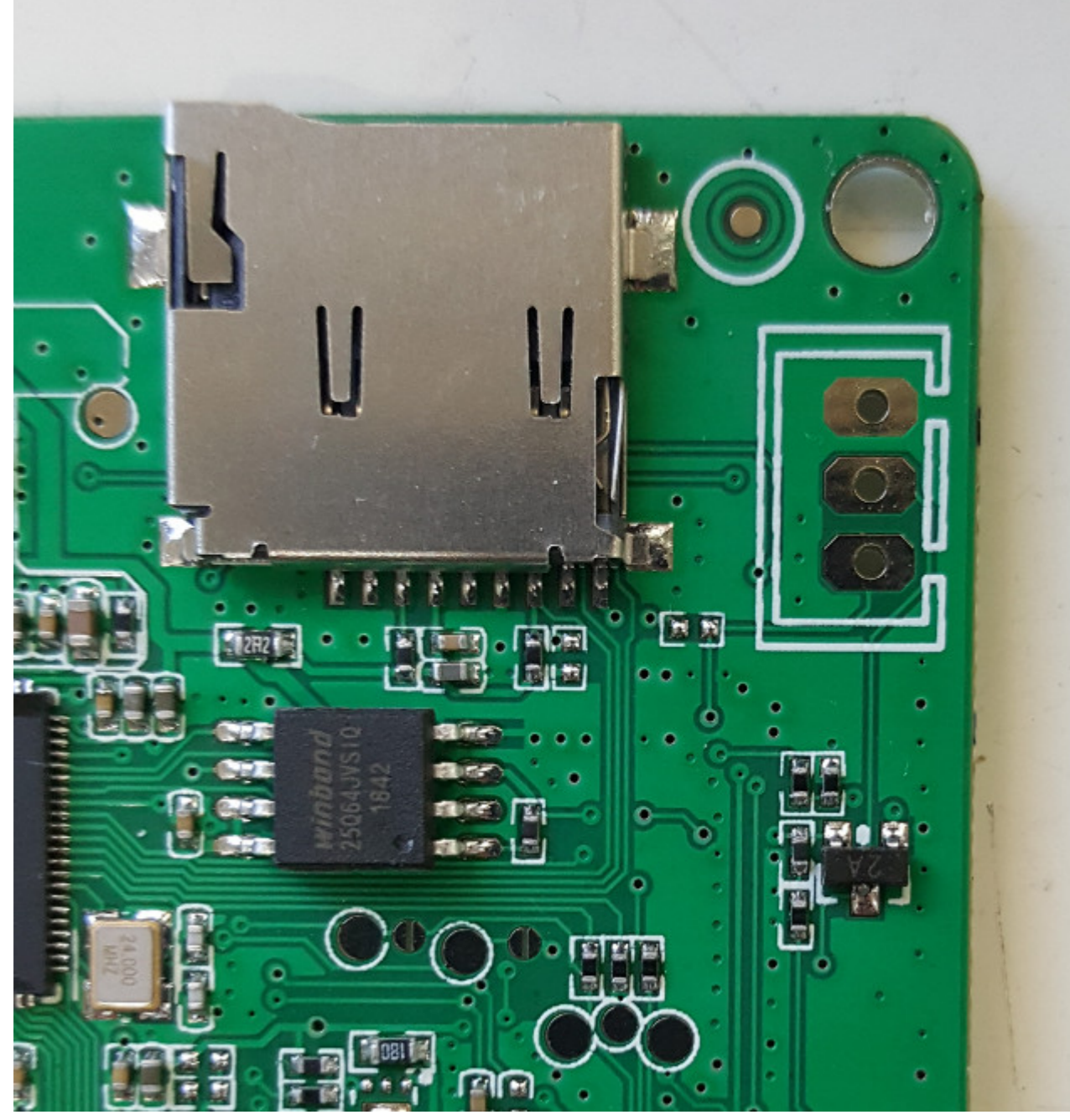
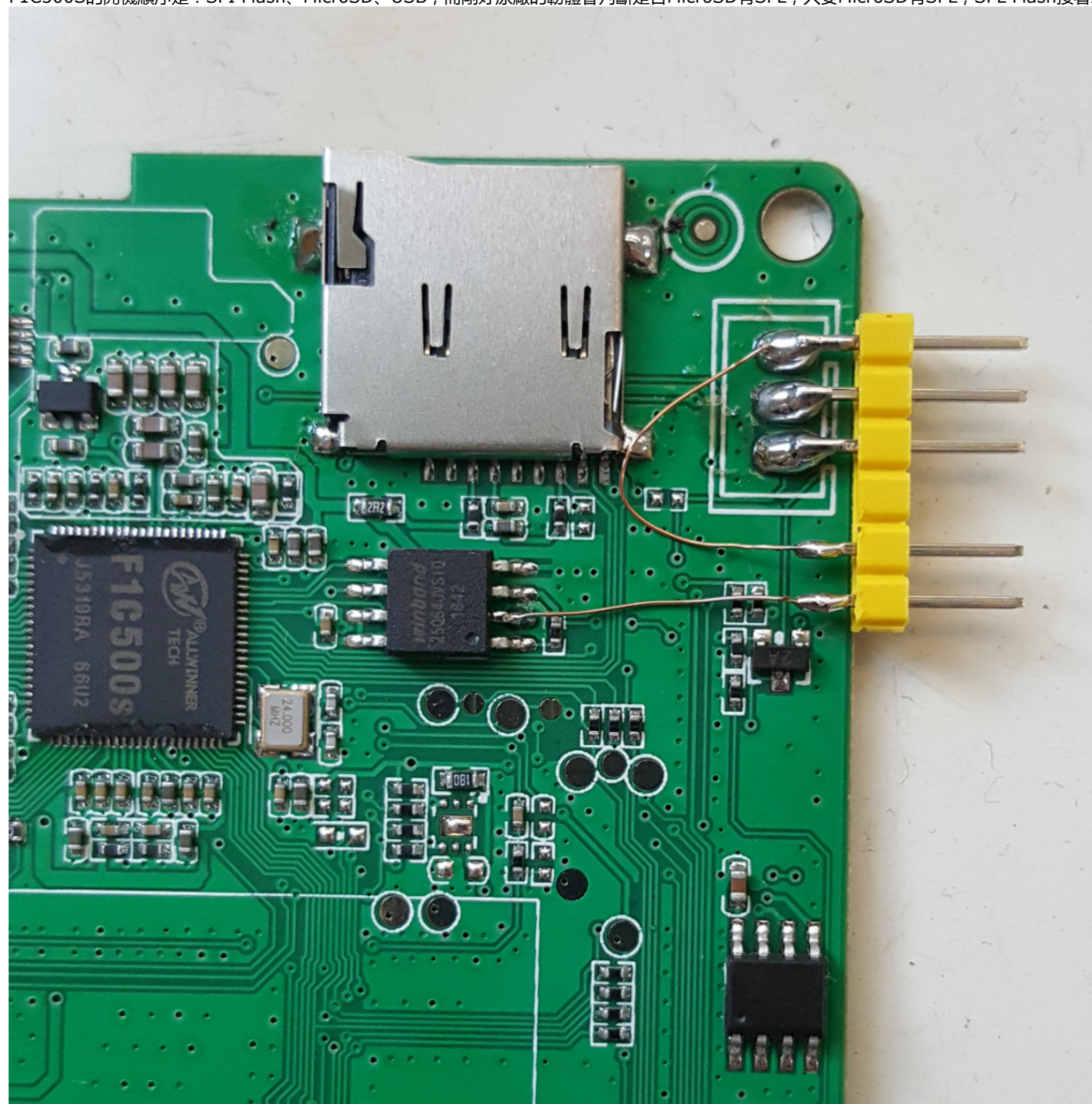


焊接UART接頭

廠商真是貼心，特別把(由上而下)GND、UART1 RX、UART1 TX拉出來，提供開發者一個友好的界面



F1C500S的開機順序是：SPI Flash、MicroSD、USB，而剛好原廠的韌體會判斷是否MicroSD有SPL，只要MicroSD有SPL，SPL Flash接著就會從MicroSD啟動，而可從特意把SPI Flash(Pin2)拉出來，因為這樣Debug Uboot會比較方便



正面



背面



側面



测试UART输出



Baudrate: 115200 bps

```
BOOT0 is starting
init dram , base      is 0x8000000
init dram , clk       is 168
init dram , access_mode is 1
init dram , cs_num    is 0x00000001
init dram , ddr8_remap is 0
init dram , sdr_ddr   is 1
init dram , width     is 16
init dram , col_width is 10
init dram , row_width is 13
init dram , bank_size is 4
init dram , cas       is 3
init dram , size      is 120
dram init succeeded,size is 32
jump to BOOT1
DBG: boot1 starting!
0
jump to kernel
Mount Parts Thread running....
partition [D] plug in.
Mount Parts Thread work now....
Mount Parts Thread work end...
*****game start*****
--1 0x00 = 0x0 --
--1 0x04 = 0x0 --
--1 0x08 = 0x0 --
--2 0x00 = 0x0 --
--2 0x04 = 0x0 --
--2 0x08 = 0x0 --
*****logo start*****c2625800*****
*****logo eng*****c2625800*****
*divider=49
b_interlace=0
=====LCD_open_cmd=====
=====lcd ID = 0x0=====
LCD_BOOT: wait to power on!!!!!!!!!!!!!!!!!!!!!!
LCD_BOOT: disable auto mode for setting!!!!!!!!!!!!!!
LCD_BOOT: id to read back is 0
=====lcd_cpu_cmdset=====
LCD_BOOT: lcd_cpu_cmdset!!!!!!!!!!!!!!!!!!!!!!
LCD_BOOT: LCD_CPU_WR_INDEX!!!!!!!!!!!!!!!!!!!!!!
LCD_BOOT: LCD_CPU_AUTO_FLUSH!!!!!!!!!!!!!!!!!!!!!!
*****nvmeminit*****
*****memory init start*****
*****memory init end*****
*****sdio init*****
SDC 0 init...
SD card found
SDC 0 identified successfully
sdcard device name: SDMMC-DISK:0
Mount Parts Thread work now....
p1
  _file_path=c29a3000
partition [F] plug in.
Mount Parts Thread work end...
  _file_path=c2a00c0d
  _file_path=c2a649c0
*****game_menu start*****
```

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