

```

1  | --- src/arch/arm32/mach-fic100s/sys-uart_orig.c 2018-12-30 21:13:22.415507314 +0800
2  | +++ src/arch/arm32/mach-fic100s/sys-uart.c 2018-12-30 22:00:43.613937647 +0800
3  | @@ -29,32 +29,32 @@
4  |     virtual_addr_t addr;
5  |     u32_t val;
6  |
7  |     /* Config GPIO4 and GPIO2 to tx0 and rx0 */
8  |     addr = 0x01208b4 + 0x00;
9  |     /* Config GPIO3 and GPIO3 to tx0 and rx0 */
10 |     addr = 0x0120800 + 0x00;
11 |     val = read32(addr);
12 |     val &= ~(0xf << ((4 & 0x7) << 2));
13 |     val |= (0x5 & 0x7) << ((4 & 0x7) << 2);
14 |     val &= ~(0xf << ((3 & 0x7) << 2));
15 |     val |= (0x5 & 0x7) << ((3 & 0x7) << 2);
16 |     write32(addr, val);
17 |
18 |     val = read32(addr);
19 |     val &= (0xf << ((2 & 0x7) << 2));
20 |     val |= (0x5 & 0x7) << ((2 & 0x7) << 2);
21 |     val |= (0x5 & 0x7) << ((2 & 0x7) << 2);
22 |     write32(addr, val);
23 |
24 |     /* Open the clock gate for uart0 */
25 |     /* Open the clock gate for uart1 */
26 |     addr = 0x01c2068;
27 |     val = read32(addr);
28 |     val |= 1 << 20;
29 |     val |= 1 << 21;
30 |     write32(addr, val);
31 |
32 |     /* Deassert uart0 reset */
33 |     /* Deassert uart1 reset */
34 |     addr = 0x01c2020;
35 |     val = read32(addr);
36 |     val |= 1 << 20;
37 |     val |= 1 << 21;
38 |     write32(addr, val);
39 |
40 |     /* Config uart0 to 115200-8-1-0 */
41 |     addr = 0x01c2500;
42 |     /* Config uart1 to 115200-8-1-0 */
43 |     addr = 0x01c2500;
44 |     write32(addr + 0x04, 0x0);
45 |     write32(addr + 0x08, 0xf7);
46 |     write32(addr + 0x10, 0x0);
47 | @@ -74,8 +74,9 @@
48 |
49 | void sys_uart_putc(char c)
50 | {
51 |     virtual_addr_t addr = 0x01c2500;
52 |     virtual_addr_t addr = 0x01c2500;
53 |     while((read32(addr + 0xf) & (0x1 << 1)) == 0);
54 |     write32(addr + 0x0, c);
55 | }
56 |
57 | --- src/arch/arm32/mach-fic100s/sast-kl131_orig.c 2018-12-31 20:22:30.760634199 +0800
58 | +++ src/arch/arm32/mach-fic100s/sast-kl131.c 2018-12-30 21:35:58.190732433 +0800
59 | @@ -60,7 +60,7 @@
60 |
61 | static void mach_logger(struct machine_t *mach, const char *buf, int count)
62 | {
63 |     virtual_addr_t virt = phys_to_virt(0x01c25000);
64 |     virtual_addr_t virt = phys_to_virt(0x01c25400);
65 |
66 |     /*
67 |      * .src/arch/arm32/mach-fic100s/romdisk/boot/sast-kl131_orig.json 2018-12-31 20:24:32.134506413 +0800
68 |      * .src/arch/arm32/mach-fic100s/romdisk/boot/sast-kl131.json 2018-12-31 11:19:48.077496340 +0800
69 |      * @ -377,7 +377,7 @@
70 |      *     "backlight": "led-pwm-bl.0"
71 |      * },
72 |      *
73 |      * "console-uart0": {
74 |      *     "uart-bus": "uart-16550.0"
75 |      * },
76 |      * "console-uart1": {
77 |      *     "uart-bus": "uart-16550.1"
78 |      * }
79 |     */
80 | }

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系统消息(115200 bps)

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V2.0.2 (Dec 31 2018 - 19:42:20) - [sast-kl131][SAST KL131 Digital Player Based On Allwinner FIC100S]
[ 0.000000] Probe device 'romdisk.0' with romdisk
[ 0.000010] Probe device 'hose' with clk-fixed
[ 0.000020] Probe device 'i2c0' with clk-fixed
[ 0.000030] Probe device 'pll-cpu' with clk-fic100s-pll
[ 0.000040] Probe device 'pll-audio' with clk-fic100s-pll
[ 0.000050] Probe device 'pll-video' with clk-fic100s-pll
[ 0.000060] Probe device 'pll-vc' with clk-fic100s-pll
[ 0.000070] Probe device 'pll-dsi' with clk-fic100s-pll
[ 0.000080] Probe device 'pll-periph' with clk-fic100s-pll
[ 0.000090] Probe device 'cpu' with clk-mux
[ 0.000100] Probe device 'hclk' with clk-divider
[ 0.000110] Probe device 'ahbl-pre-div' with clk-divider
[ 0.000120] Probe device 'mux-ahbl' with clk-mux
[ 0.000130] Probe device 'ahbl' with clk-ratio
[ 0.000140] Probe device 'apbl' with clk-ratio
[ 0.000150] Probe device 'gate-bus-uart0' with clk-gate
[ 0.000160] Probe device 'gate-bus-uart1' with clk-gate
[ 0.000170] Probe device 'gate-bus-uart2' with clk-gate
[ 0.000180] Probe device 'link-uart0' with clk-link
[ 0.000190] Probe device 'link-uart1' with clk-link
[ 0.000200] Probe device 'link-uart2' with clk-link
[ 0.000210] Probe device 'gate-bus-i2c0' with clk-gate
[ 0.000220] Probe device 'gate-bus-i2c1' with clk-gate
[ 0.000230] Probe device 'gate-bus-i2c2' with clk-gate
[ 0.000240] Probe device 'link-i2c0' with clk-link
[ 0.000250] Probe device 'link-i2c1' with clk-link
[ 0.000260] Probe device 'link-i2c2' with clk-link
[ 0.000270] Probe device 'gate-bus-spi0' with clk-gate
[ 0.000280] Probe device 'gate-bus-spi1' with clk-gate
[ 0.000290] Probe device 'link-spi0' with clk-link
[ 0.000300] Probe device 'link-spi1' with clk-link
[ 0.000310] Probe device 'mux-timer0' with clk-mux
[ 0.000320] Probe device 'ratio-timer0' with clk-ratio
[ 0.000330] Probe device 'link-timer0' with clk-link
[ 0.000340] Probe device 'mux-timer1' with clk-mux
[ 0.000350] Probe device 'ratio-timer1' with clk-ratio
[ 0.000360] Probe device 'link-timer1' with clk-link
[ 0.000370] Probe device 'mux-timer2' with clk-mux
[ 0.000380] Probe device 'ratio-timer2' with clk-ratio
[ 0.000390] Probe device 'link-timer2' with clk-link
[ 0.000400] Probe device 'link-pwm' with clk-link
[ 0.000410] Probe device 'link-adt' with clk-link
[ 0.000420] Probe device 'mux-defe' with clk-mux
[ 0.000430] Probe device 'div-defe' with clk-divider
[ 0.000440] Probe device 'gate-defe' with clk-gate
[ 0.000450] Probe device 'gate-bus-defe' with clk-gate
[ 0.000460] Probe device 'link-defe' with clk-link
[ 0.000470] Probe device 'mux-debe' with clk-mux
[ 0.000480] Probe device 'div-debe' with clk-divider
[ 0.000490] Probe device 'gate-debe' with clk-gate
[ 0.000500] Probe device 'gate-bus-debe' with clk-gate
[ 0.000510] Probe device 'link-debe' with clk-link
[ 0.000520] Probe device 'mux-tcon' with clk-mux
[ 0.000530] Probe device 'gate-tcon' with clk-gate
[ 0.000540] Probe device 'gate-bus-tcon' with clk-gate
[ 0.000550] Probe device 'link-tcon' with clk-link
[ 0.000560] Probe device 'reset-fic100s.0' with reset-fic100s
[ 0.000570] Probe device 'reset-fic100s.1' with reset-fic100s
[ 0.000580] Probe device 'reset-fic100s.2' with reset-fic100s
[ 0.000590] Probe device 'irq-fic100s.0' with irq-fic100s
[ 0.000600] Probe device 'irq-fic100s-gpio.0' with irq-fic100s-gpio
[ 0.000610] Probe device 'irq-fic100s-gpio.1' with irq-fic100s-gpio
[ 0.000620] Probe device 'irq-fic100s-gpio.2' with irq-fic100s-gpio
[ 0.000630] Probe device 'gpio-fic100s.0' with gpio-fic100s
[ 0.000640] Probe device 'gpio-fic100s.1' with gpio-fic100s
[ 0.000650] Probe device 'gpio-fic100s.2' with gpio-fic100s
[ 0.000660] Probe device 'gpio-fic100s.3' with gpio-fic100s
[ 0.000670] Probe device 'gpio-fic100s.4' with gpio-fic100s
[ 0.000680] Probe device 'pwm-fic100s.0' with pwm-fic100s
[ 0.000690] Probe device 'pwm-fic100s.1' with pwm-fic100s
[ 0.000700] Probe device 'ce-fic100s-timer.0' with ce-fic100s-timer
[ 0.000710] Probe device 'ce-fic100s-timer.0' with ce-fic100s-timer
[ 0.006447] Probe device 'i2c-fic100s.0' with i2c-fic100s
[ 0.011954] Probe device 'i2c-fic100s.1' with i2c-fic100s
[ 0.017461] Probe device 'i2c-fic100s.2' with i2c-fic100s
[ 0.022558] Probe device 'uart-16550.0' with uart-16550
[ 0.028072] Probe device 'uart-16550.1' with uart-16550
[ 0.033346] Probe device 'uart-16550.2' with uart-16550
[ 0.035336] Probe device 'spi-fic100s.0' with spi-fic100s
[ 0.043891] Probe device 'spi-fic100s.1' with spi-fic100s
[ 0.049310] Probe device 'spi-flash.0' with spi-flash
[ 0.054308] Probe device 'wdog-fic100s.0' with wdog-fic100s
[ 0.059852] Probe device 'key-fic100s-iradc.0' with key-fic100s-iradc

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[ 0.066237] Probe device 'led-gpio.0' with led-gpio
[ 0.071072] Probe device 'ledtrig-heartbeat.0' with ledtrig-heartbeat
[ 0.074743] Probe device 'led-pwm-bl.0' with led-pwm-bl
[ 0.302363] Probe device 'fb-f1c100s.0' with fb-f1c100s
[ 0.307350] Probe device 'console-uart.1' with console-uart
Press any key to stop autoboot: 0.000
xboot: /$
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[返回上一頁 \(/../handheld.htm\)](#)