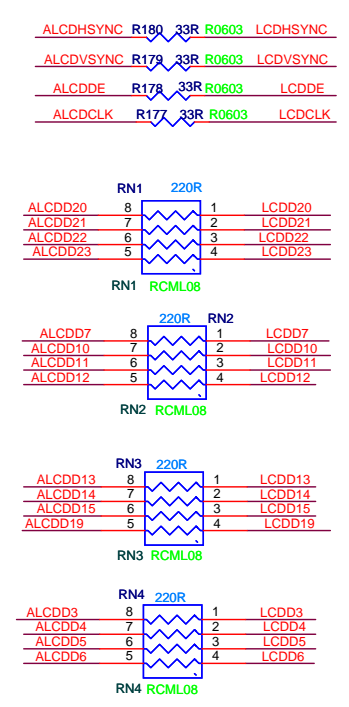
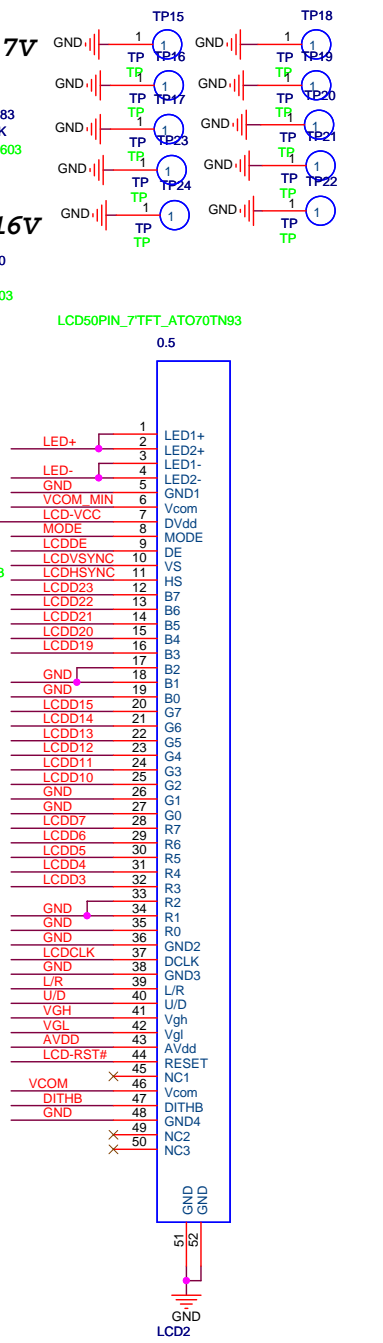
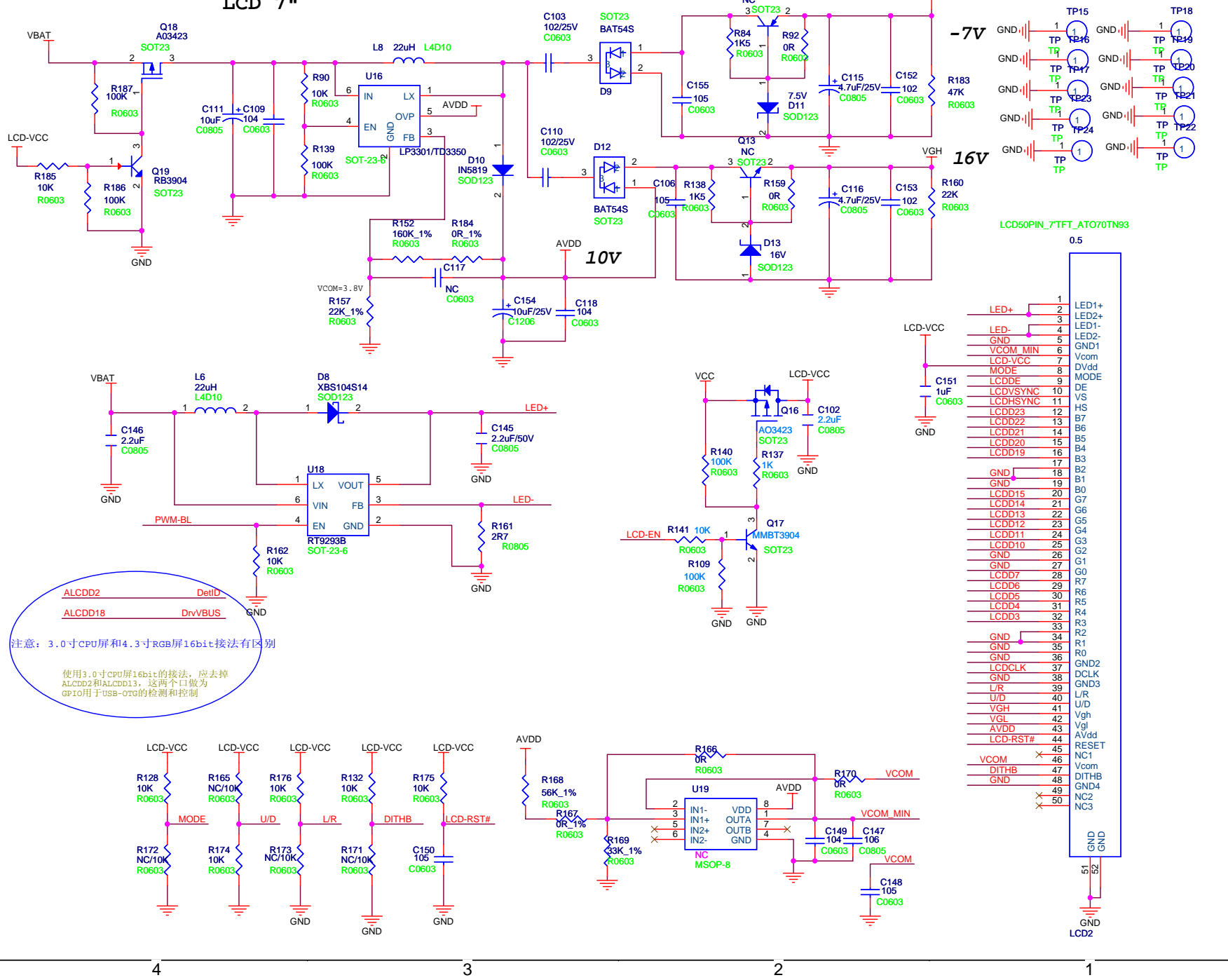


LCD 7"

- 3.5 ALCDD[2:7] >>> ALCDD[2:7]
- 3 ALCDD[10:15] >>> ALCDD[10:15]
- 3.5 ALCDD[18:23] >>> ALCDD[18:23]
- 3 ALCDHSYNC >>> ALCDHSYNC
- 3 ALCDVSYSYNC >>> ALCDVSYSYNC
- 3 ALCDDDE >>> ALCDDDE
- 3 ALCDCCLK >>> ALCDCCLK
- 3 PWM-BL >>> PWM-BL
- 3 DetID >>> DetID
- 3 DrvVBUS >>> DrvVBUS
- 3 LCD-EN >>> LCD-EN



注意: 3.0寸CPU屏和4.3寸RGB屏16bit接法有区别
 使用3.0寸CPU屏16bit的接法, 应去掉ALCDD2和ALCDD13, 这两个口做为GPIO用于USB-otg的检测和控制



Notes: Unless Otherwise Stated

Scheme Spec:

FLASH:NOR, 4M, 3.3V
DRAM: SDR, 8M*16, 3.3V
Key: 7, Left, Right, Vol+, Vol-, OK, MENU, POWER
Power:USB, 5V, 2A; BAT, 4.2V, 1000mA
USB: USB-OTG
Card: SD/MMC(一线卡)
TVout:CVBS
其它: Headphone, MIC, FM, IR, RTC, G-Sensor
LCD仅支持480*272以下分辨率

Power Supply:

VCC: 3.3V, 700mA, Periphery device & SDR
VDD: 1.4V, 700mA, Core
AVCC&VMIC: 3V, 300mA, Analog

Schematics Index:

- 01 COVER
- 02 PIO ASSIGNMENT
- 03 MAIN CHIP &SDRAM
- 04 HP & FM & MIC & RTC
- 05 USB & POWER
- 06 KEY & IR & CARD & G-SENSOR
- 07 LCD POWER
- 08 LCD 3"
- 09 LCD 4.3"

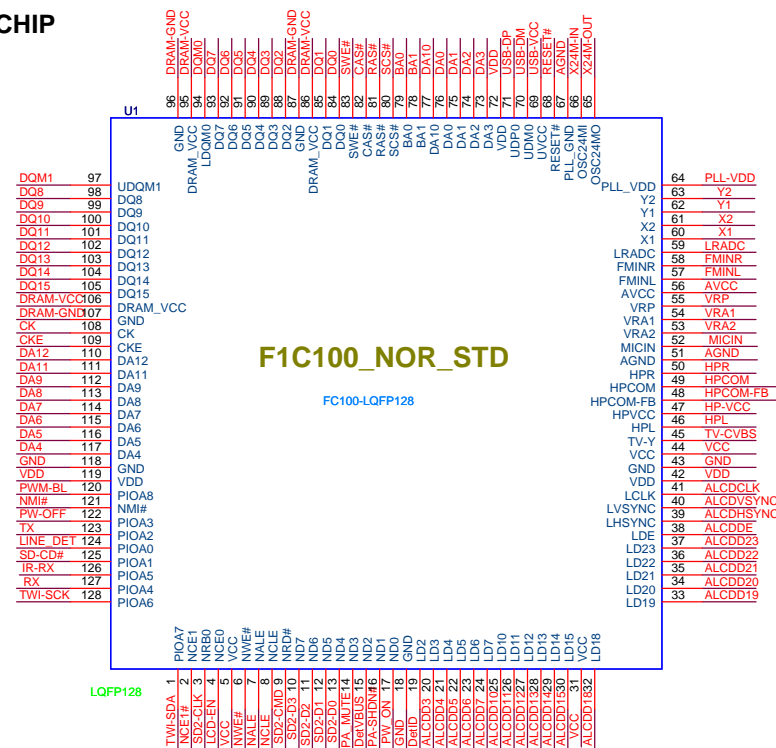
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F1C100_NOR_STD_V1.3		2011.05.12			
F1C100_NOR_STD_V1.4		2011.06.22			

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PIO ASSIGNMENT

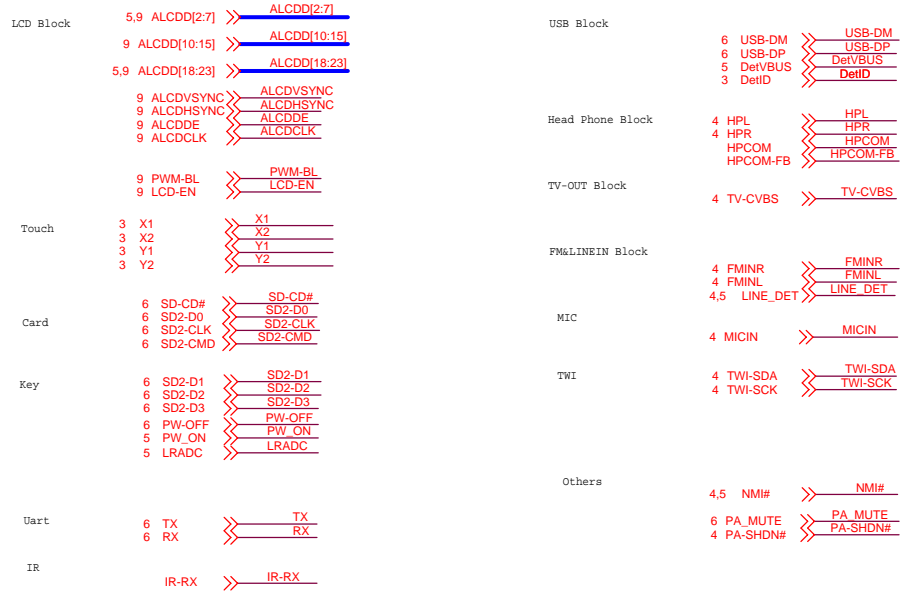
Function	Pin Name	Default Function	Multi0	Multi1	Multi2	Multi3	Function	Notes	Default Status	Circuit Status	Function description	I/O	
NAND	NWE#	PIOC0	NWE#	SPI0_MOSI			SPI0_MOSI	NOR					
	ALE	PIOC1	ALE	SPI0_MISO			SPI0-MISO						
	CLE	PIOC2	CLE	SPI0_CLK			SPI0_CLK						
	NCE1	PIOC3	NCE1	SPI0_CS			SPI0-CS		pull-up				
	NCE0	PIOC4	NCE0				LCD-EN	LCD	pull-up	Pull high	high--shutdown, low--normal	Output	
	NRD#	PIOC5	NRD#	SDC2_CMD			SDC2_CMD	CARD					
	NRB0	PIOC6	NRB0	SDC2_CLK			SDC2_CLK		pull-up				
	ND0	PIOC8	ND0	SDC0_D0			PW_ON	POWER		pull high	high--power on, low-power down	output	
	ND1	PIOC9	ND1	SDC0_D1			PA-SHDN#	PA		pull high	low--shutdown, high--normal	output	
	ND2	PIOC10	ND2	SDC0_D2			DetVBUS	USB		pull low	high--Vbus enable, low--no connection	input	
	ND3	PIOC11	ND3	SDC0_D3			TV-EN	CVBS OUT				output	
	ND4	PIOC12	ND4	SDC2_D0			SDC2_D0	CARD					
	ND5	PIOC13	ND5	SDC2_D1			SDC2_D1						
	ND6	PIOC14	ND6	SDC2_D2			SDC2_D2		KEY				
	ND7	PIOC15	ND7	SDC2_D3			SDC2_D3						
GPIOA	PIOA0	DBG_MS	SDC0_D1	DBG_MS	SPI1_CS0	EINT0	GSENSOR-INT	GSENSOR-INT					
	PIOA1	DBG_DI	SDC0_D0	DBG_DI	SPI1_MOSI	EINT1	SD-CD#	CARD			interrupt in, pulse	input	
	PIOA2	PIOA2	SDC0_CLK	UART0_TX	SPI1_CLK		TX	TX					
	PIOA3	DBG_DO	SDC0_CMD	DBG_DO	SPI1_MISO		PW-OFF	POWER		pull low	high--power down, low--normal	input	
	PIOA4	PIOA4	SDC0_D3	UART0_RX		EINT2	CHARGE	CHARGE DET					
	PIOA5	DBG_CK	SDC0_D2	DBG_CK	IR_RX	EINT3	IR-RX	IR					
	PIOA6	PIOA6	TWI0_SCK	NCE2			TWI-SCK	IIC	pull-up				
	PIOA7	PIOA7	TWI0_SDA	NCE3			TWI-SDA		pull-up				
PIOA8	PIOA8	PWM0	SPI1_CS1			EINT4	PWM-BL	LCD					
TP	X1	X1	GPIOE0	EINT0			X1	TP					
	X2	X2	GPIOE1	EINT1			X2						
	Y1	Y1	GPIOE2	EINT2			Y1						
	Y2	Y2	GPIOE3	EINT3			Y2						
LCD	LCD2	PIOD2	LCD2				DetID	USB-OTG				input	
	LCD13	PIOD13	LCD13				DrvVBUS				选择3.0寸CPU屏16bit接法时使用此IO	output	
	LCD18	PIOD22	LCD18								选择4.3寸RGB屏16bit接法时使用此IO	output	

MAIN CHIP

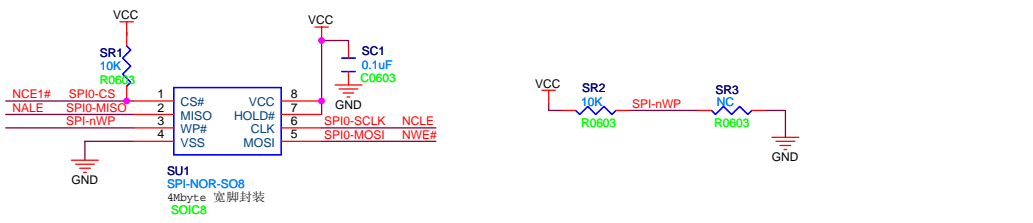


F1C100_NOR_STD

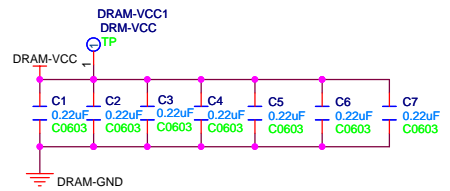
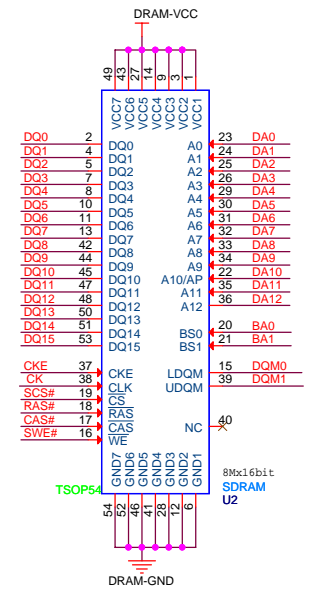
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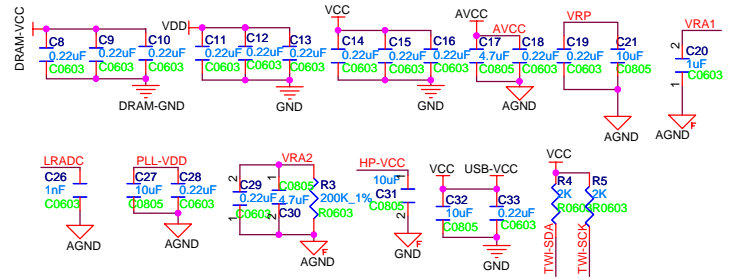
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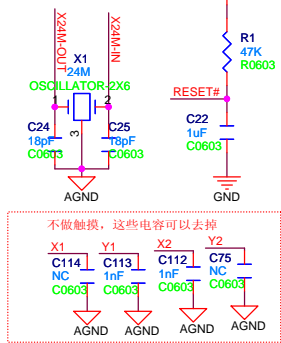
SDR



BESIDE MAIN CHIP

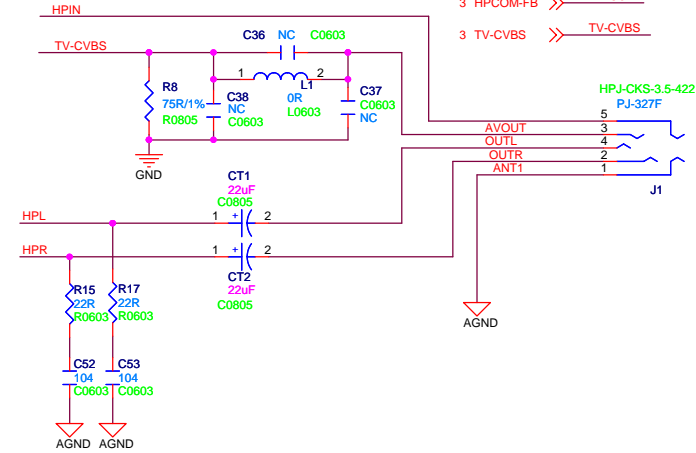


Crystal

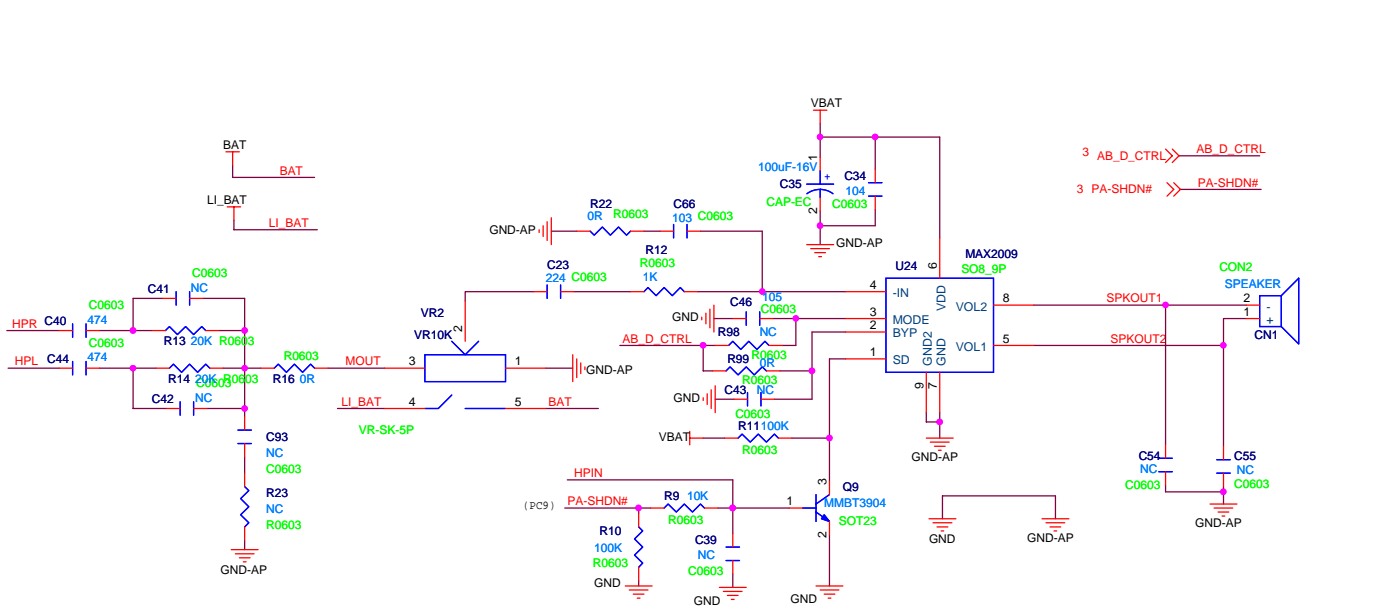


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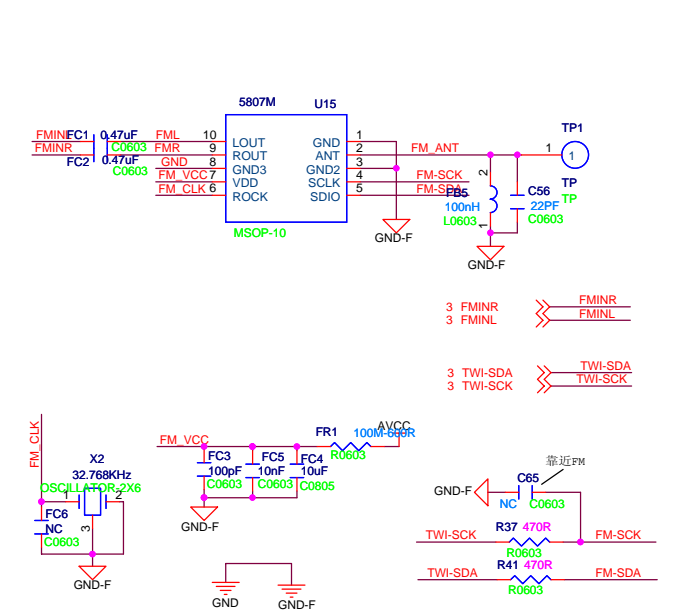
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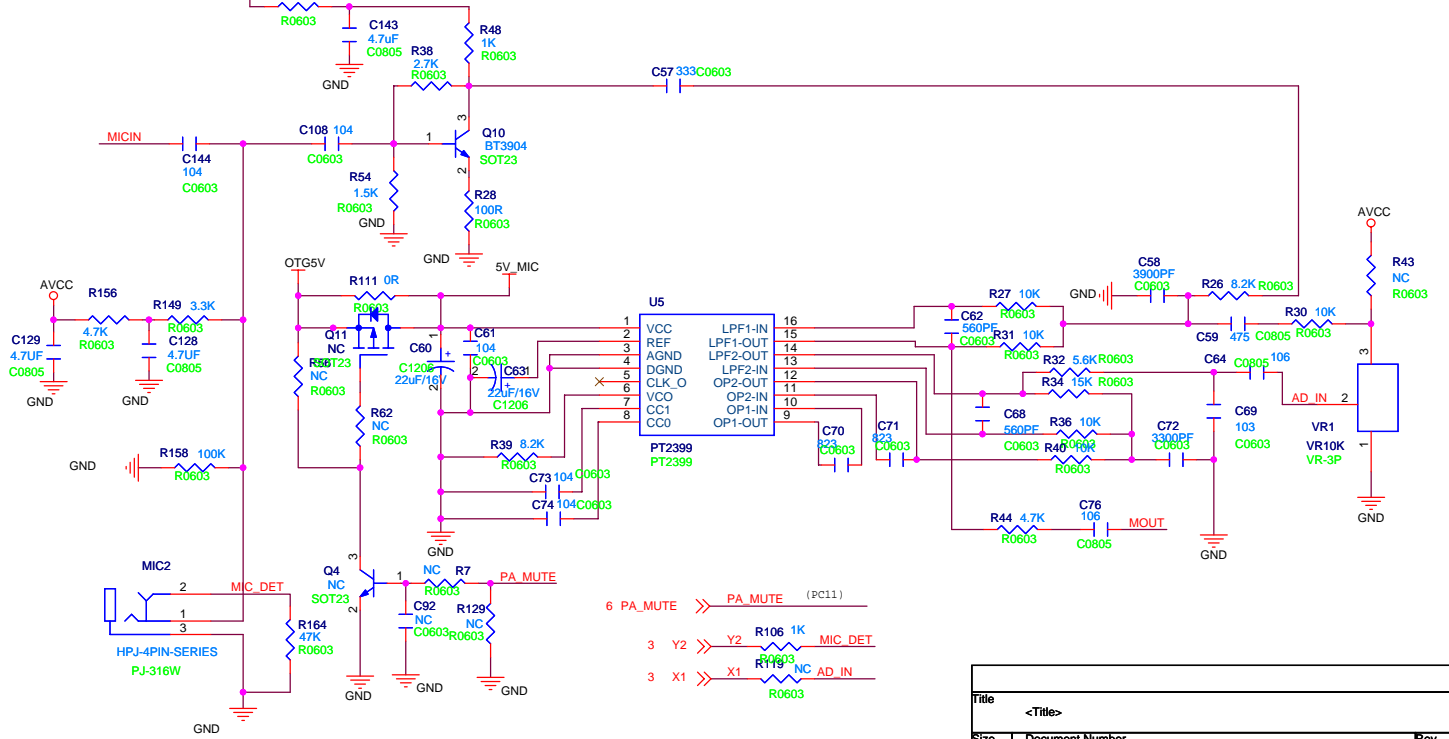
Speaker



FM Module

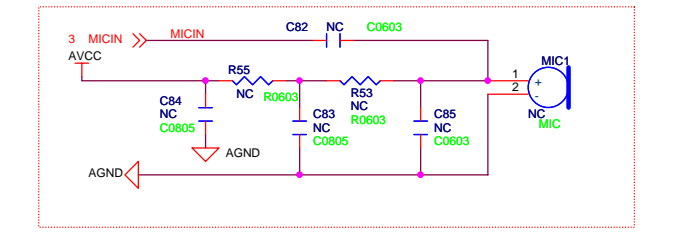


MIC_REV



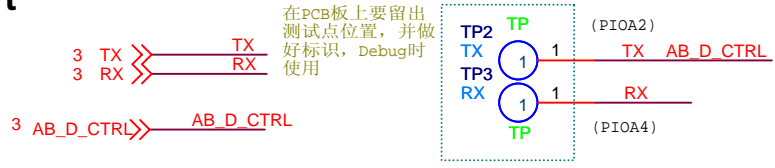
MIC

看客户要求是否保留，如果不需要机器自带录音，就可以去掉，然后通过麦克风孔录音



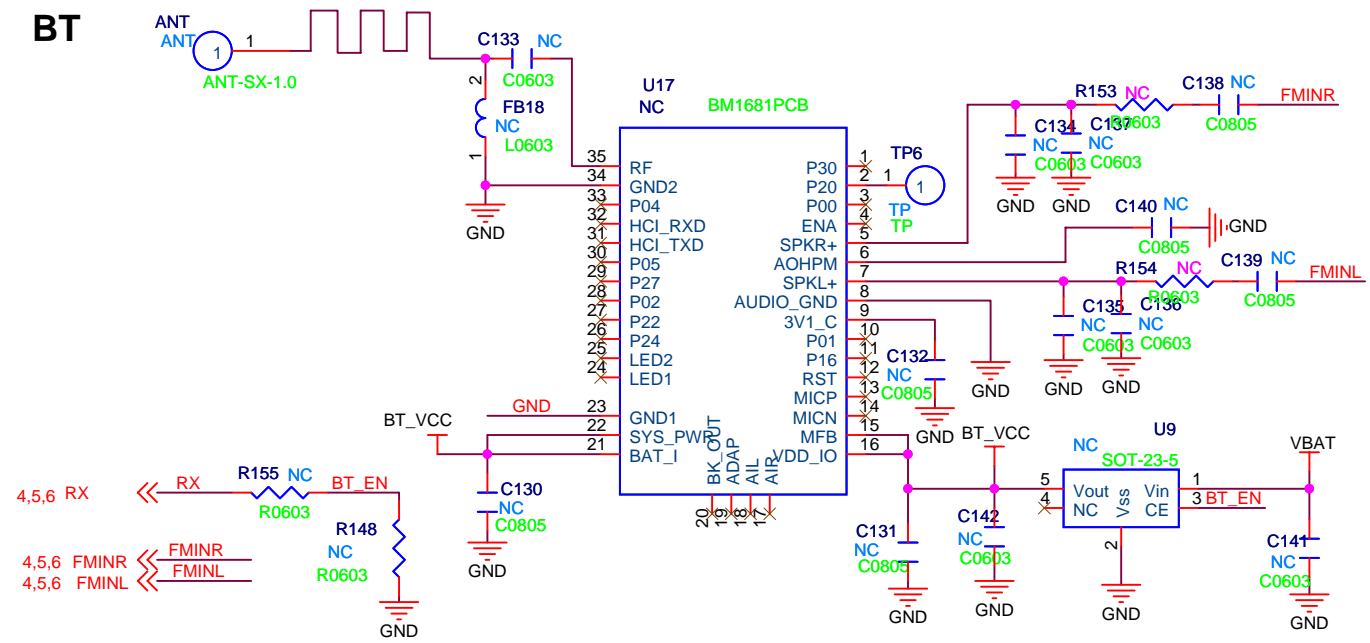
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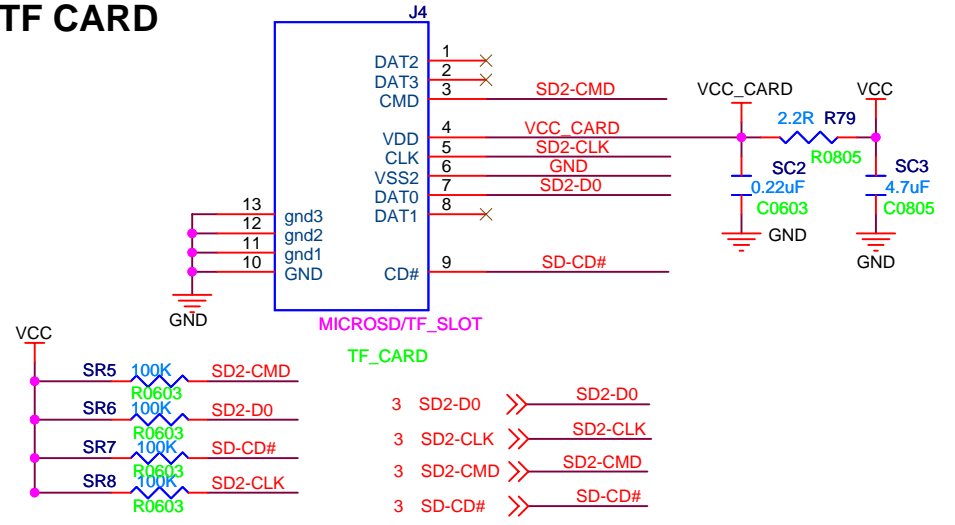


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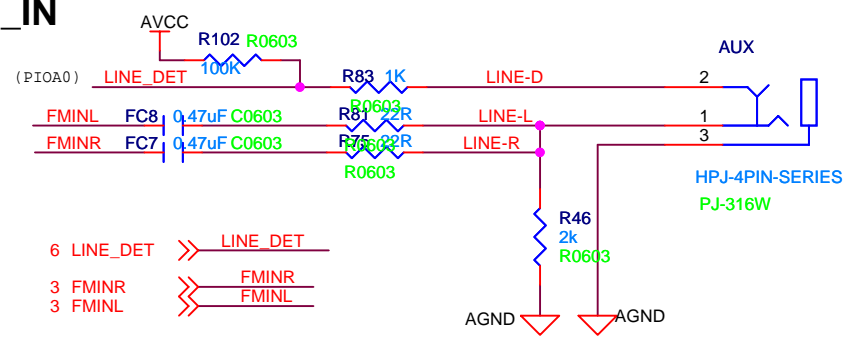
BT



TF CARD

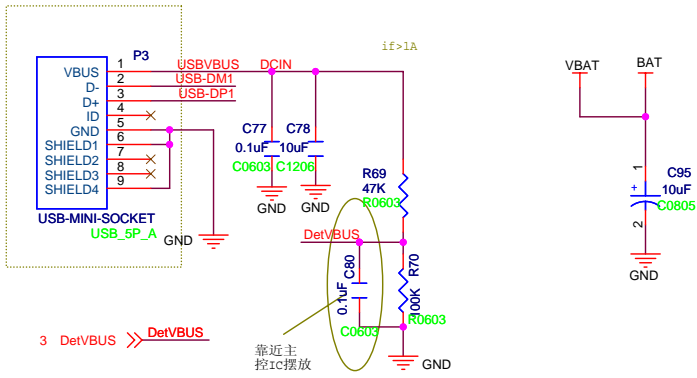


LINE_IN

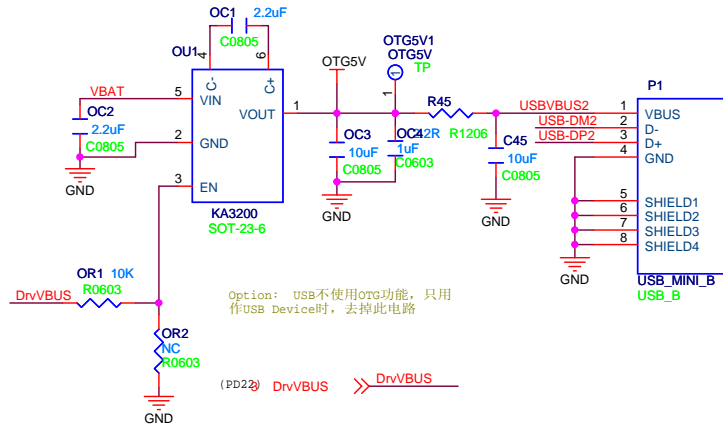


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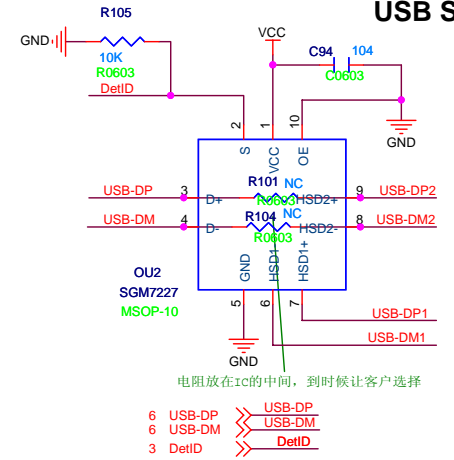
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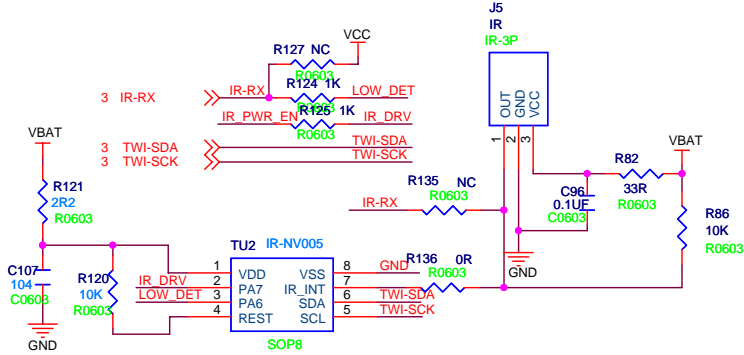
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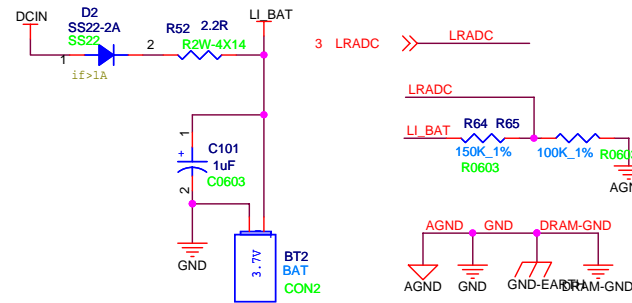
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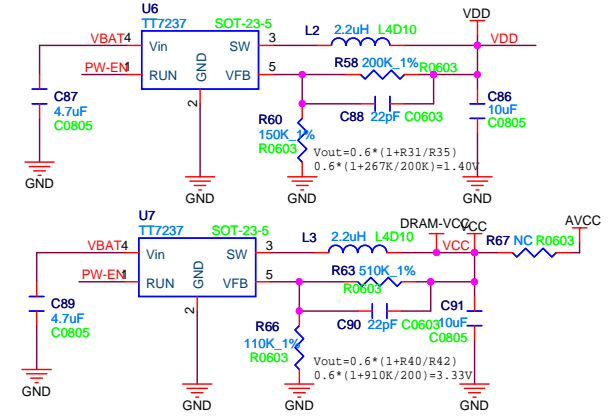
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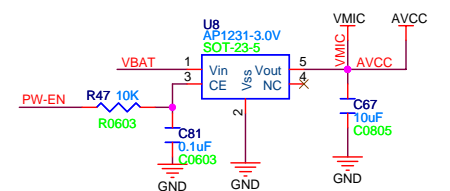
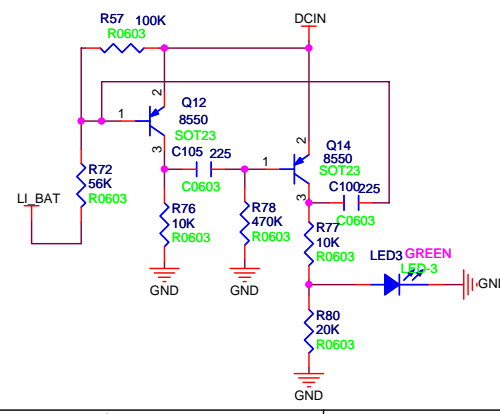
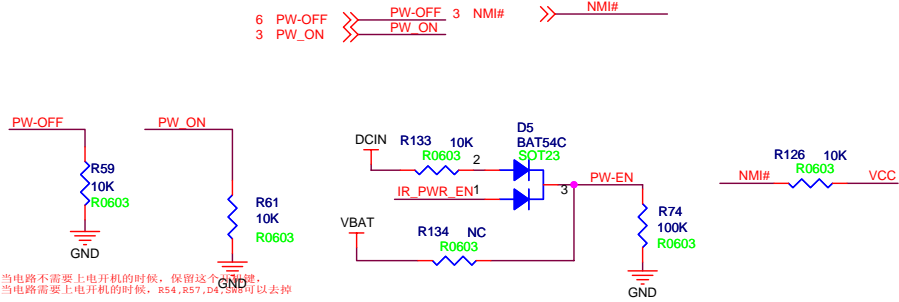
LI BAT



POWER

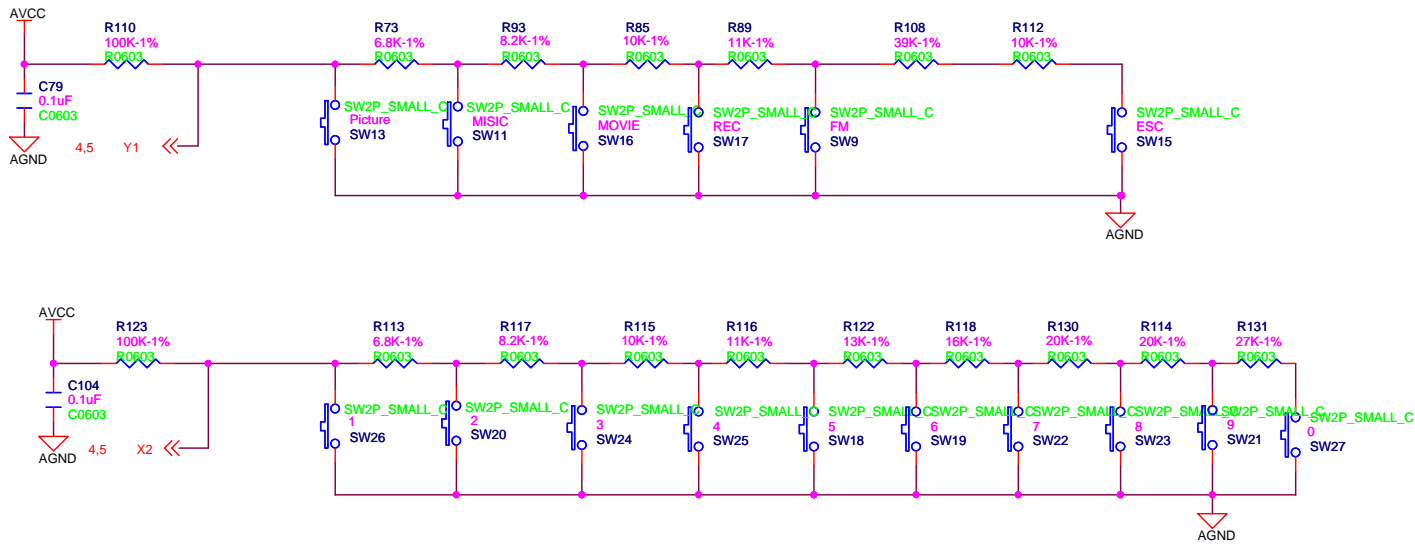


ON/OFF



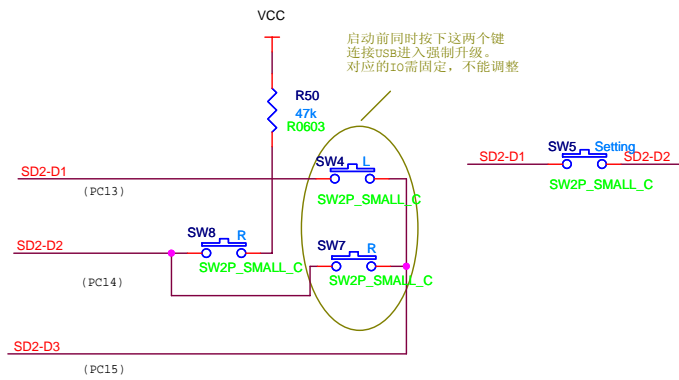
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KEY1



KEY2

- 3 SD2-D1
- 3 SD2-D2
- 3 SD2-D3



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