

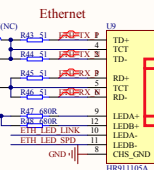
板上使用的是MX35LF1GE4B-Z4 (1Gbit)

LCD_RGB_40P

VLED+	J1	LEDK
VLED+	1	LEDA
VLED+	2	LEDB
VLED+	3	VCC
VLED+	4	VCC
VLED+	5	VCC
VLED+	6	R0
VLED+	7	R1
VLED+	8	R2
VLED+	9	R3
VLED+	10	R4
VLED+	11	R5
VLED+	12	R6
VLED+	13	R7
VLED+	14	R8
VLED+	15	G1
VLED+	16	G2
VLED+	17	G3
VLED+	18	G4
VLED+	19	G5
VLED+	20	G6
VLED+	21	G7
VLED+	22	B0
VLED+	23	B1
VLED+	24	B2
VLED+	25	B3
VLED+	26	B4
VLED+	27	B5
VLED+	28	B6
VLED+	29	B7
VLED+	30	GN
VLED+	31	LCD_CLK
VLED+	32	GN
VLED+	33	GN
VLED+	34	GN
VLED+	35	GN
VLED+	36	GN
VLED+	37	GN
VLED+	38	GN
VLED+	39	GN
VLED+	40	GN

GPIO

V3V3	J1	1	UART2_TX
V3V3	J1	2	UART2_RX
V3V3	J1	3	PWM0
V3V3	J1	4	PWM1
V3V3	J1	5	SPI_SCK
V3V3	J1	6	PWM0
V3V3	J1	7	SPI_MISO
V3V3	J1	8	UART0_TX
V3V3	J1	9	UART0_RX
V3V3	J1	10	PWM1
V3V3	J1	11	SPI_MOSI
V3V3	J1	12	UART0_TX
V3V3	J1	13	SPI_MISO
V3V3	J1	14	UART0_RX
V3V3	J1	15	SPI_CS
V3V3	J1	16	PWM1
V3V3	J1	17	15V



Title	Cherry-PC-V3S_V1.2		
Size	Number	Revision	YXW
A2			
Date:	2020/10/30	Sheet of	
File:	C:\Users\Cherry\PC-V3S_V1.2\Sch\	Drawn by:	XLW