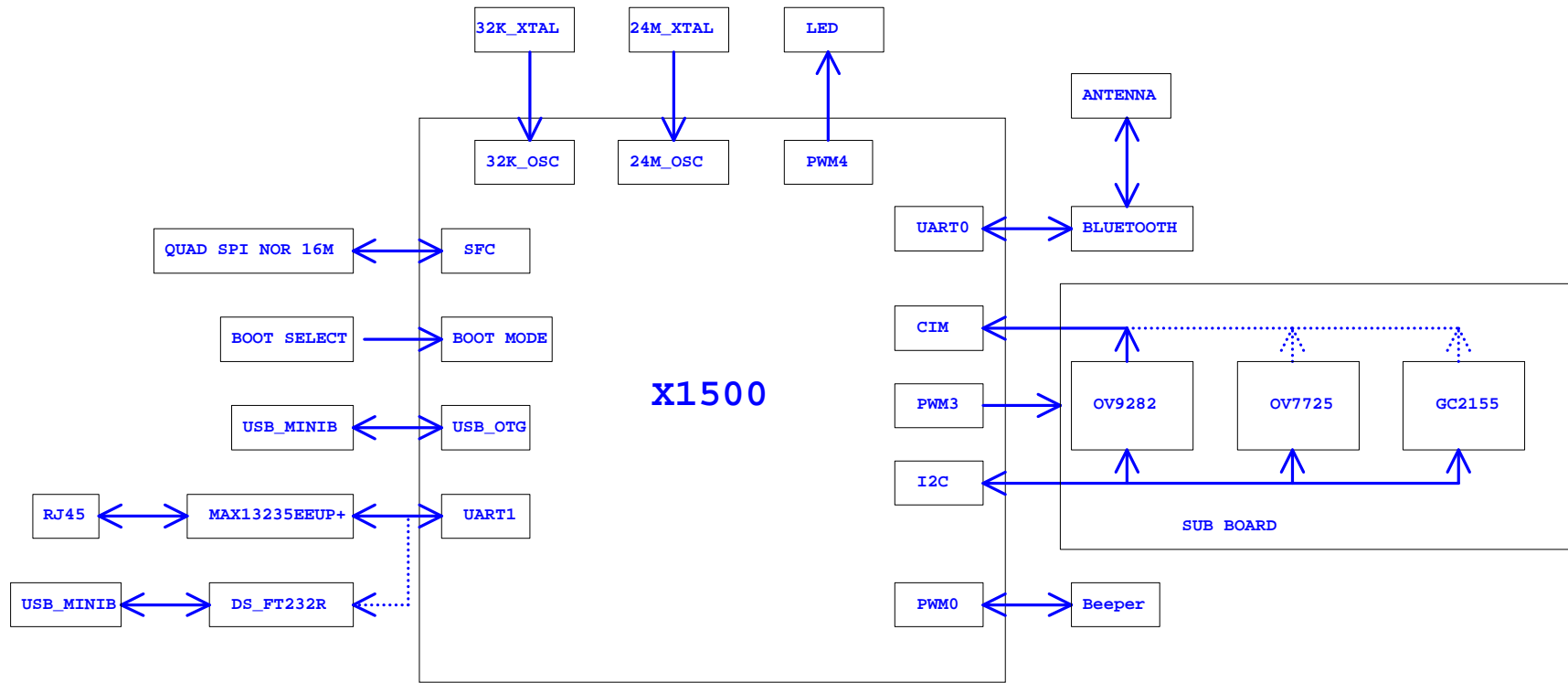


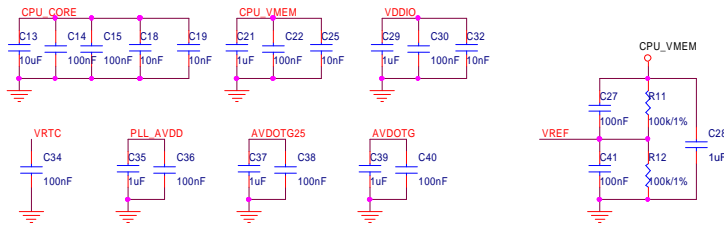
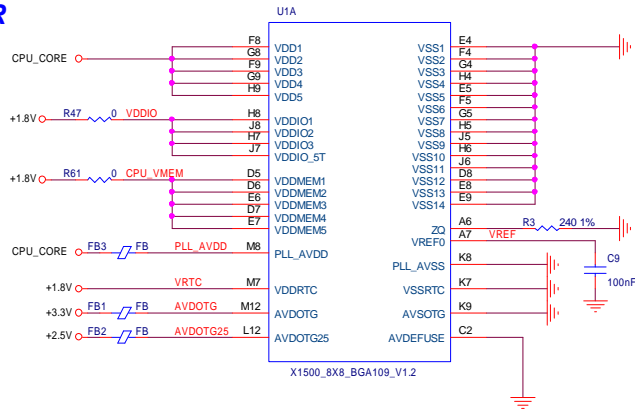
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SYSTEM ARCHITECTURE	2
CPU_PWR/POWER SUPPLY	3
BOOT/TRIGGER/BEEPER	4
HOST INTERFACE	5
BT	6
CAMERA	7
HISTORY	8

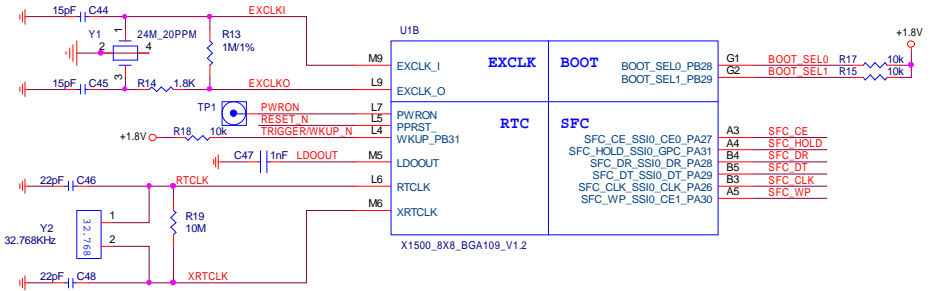


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RD_X1500_YAK			
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CPU_POWER

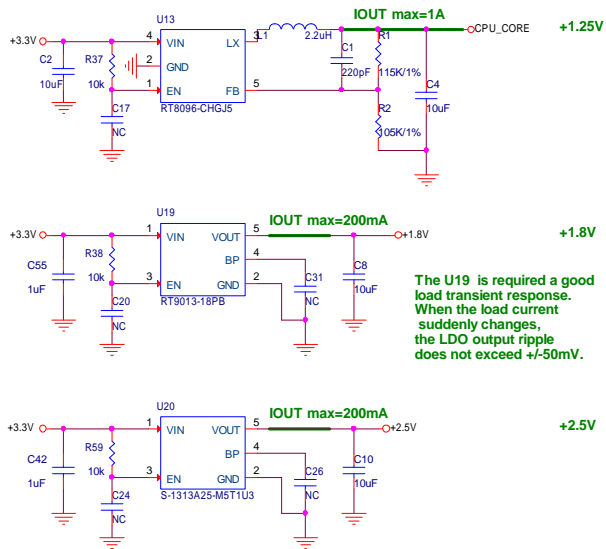


BOOT & CLOCK



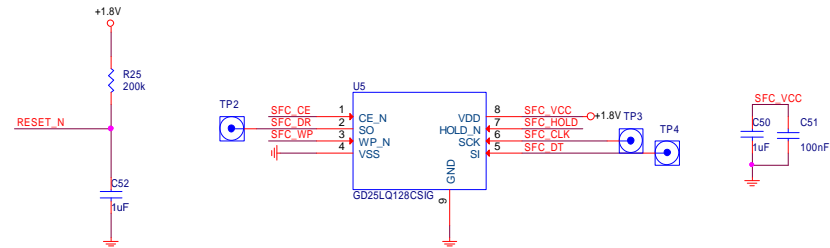
TRIGGER/WKUP_N >> 4.5
 BOOT_SEL0 >> 4.5
 RESET_N >> 4.5

MAIN POWER

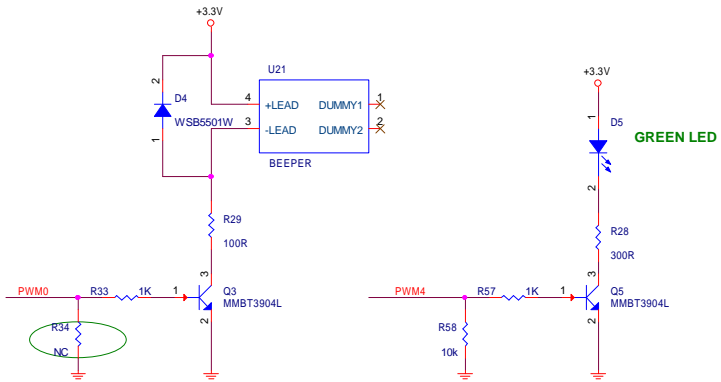


The U19 is required a good load transient response. When the load current suddenly changes, the LDO output ripple does not exceed +/-50mV.

RC_RESET & SFC

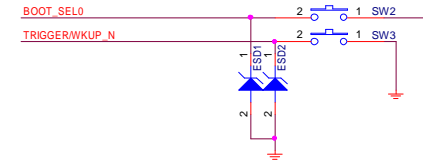


BEEPER



R34 should be added to the BEEPER circuit when PWM0 is not connected to PC25

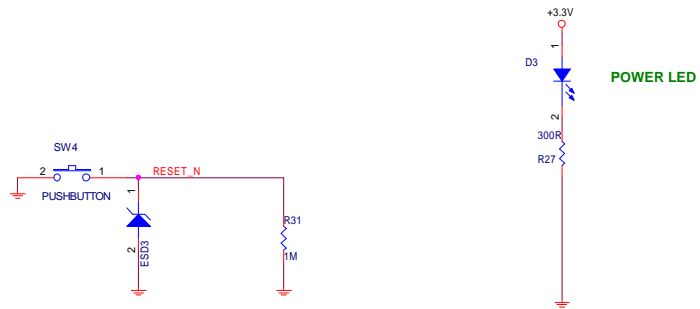
BOOT CFG & TRIGGER

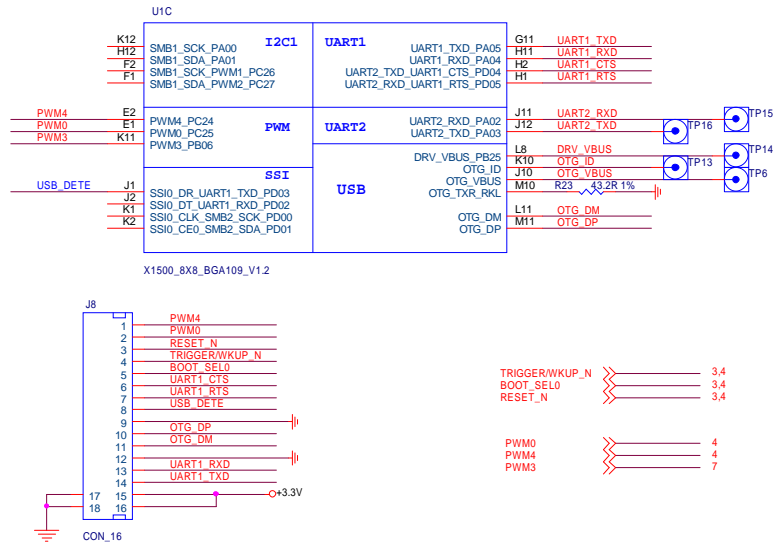


Boot Mode Select

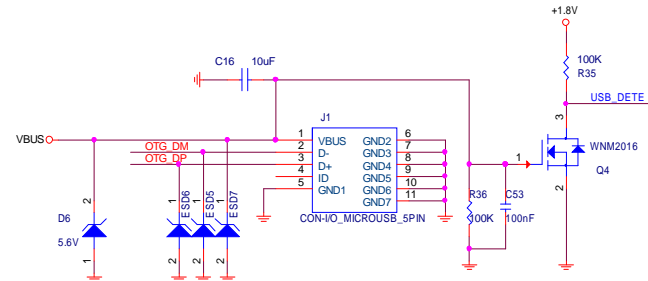
BOOT_SEL0	BOOT FROM
HIGH	SFC Boot @ 24M(default)
LOW	USB Boot @ 24M

RESET & INDICATE

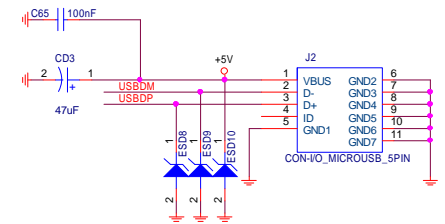




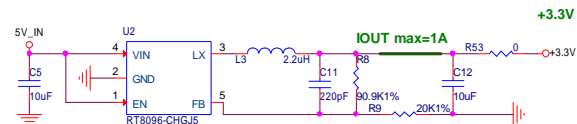
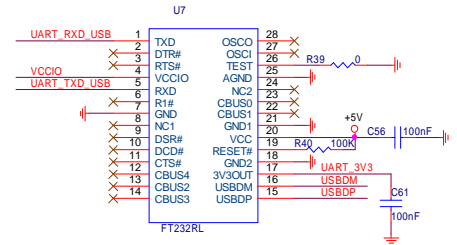
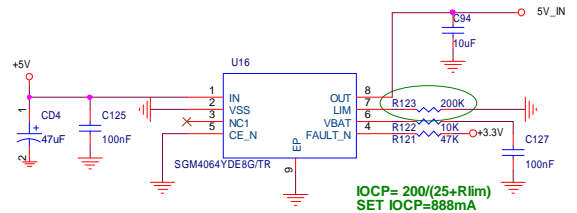
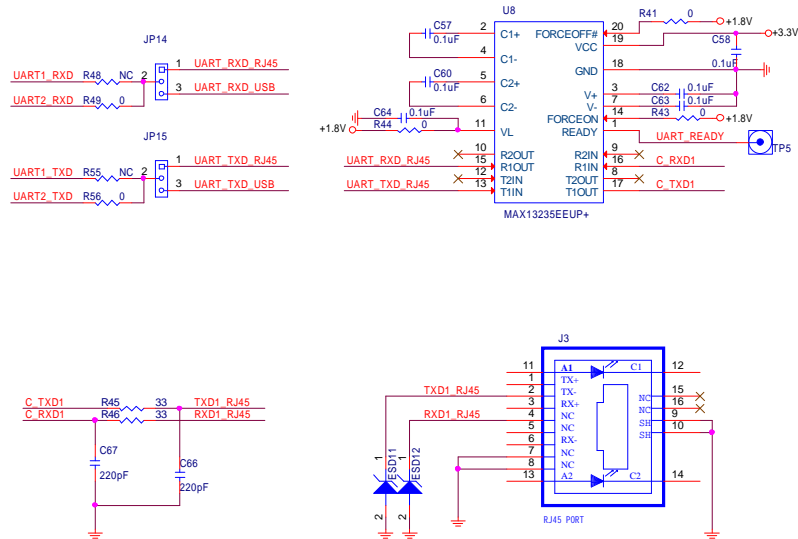
USB



UART TO USB & POWER SUPPLY



UART TO RJ45



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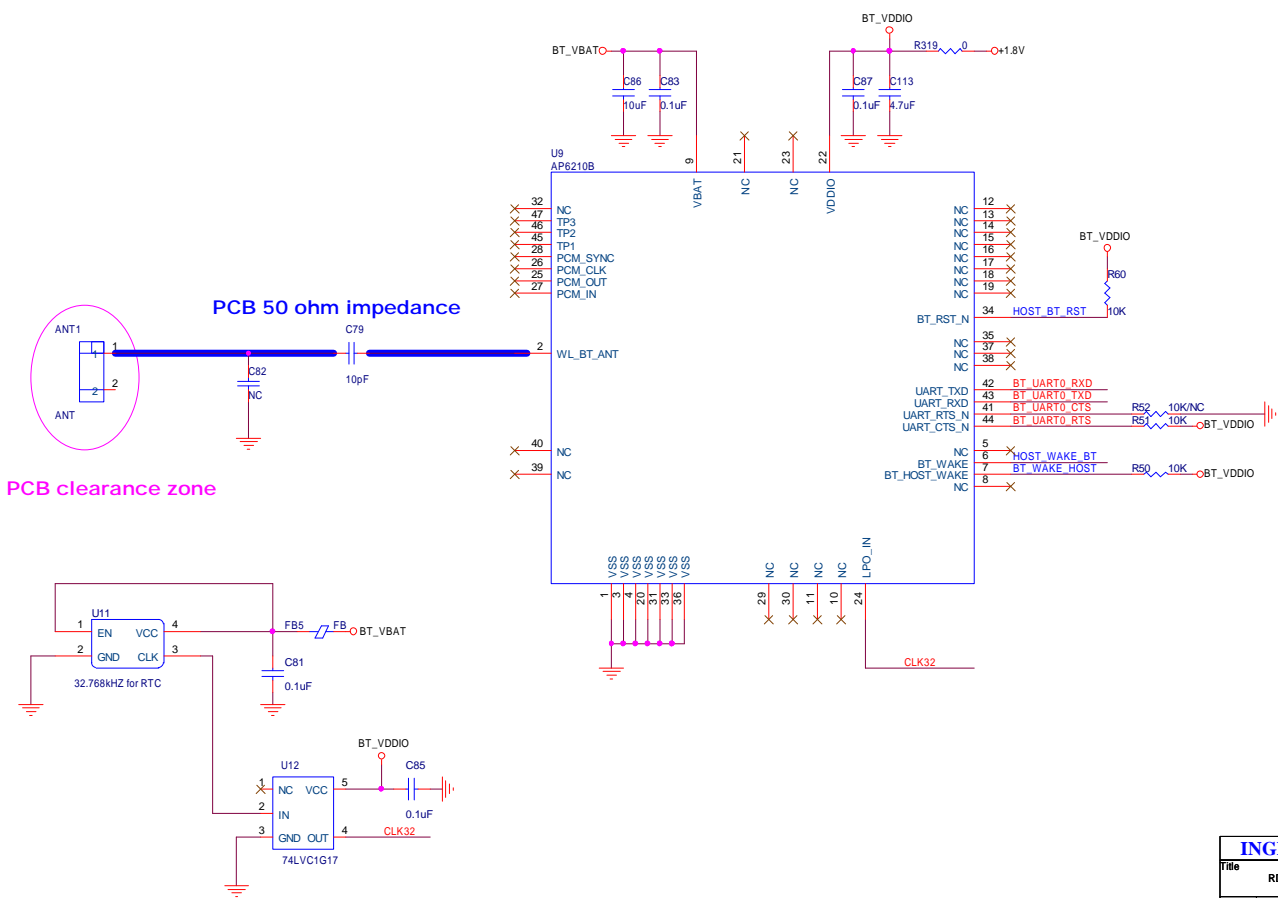
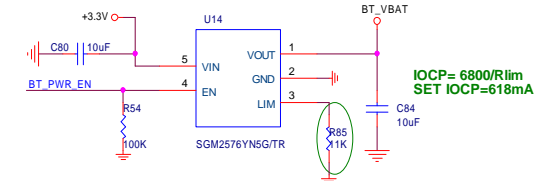
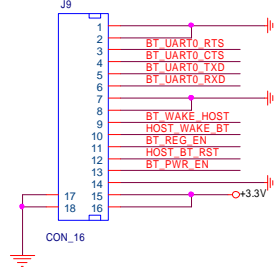
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BLUETOOTH

UID

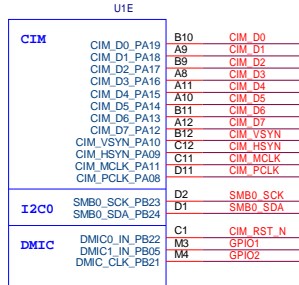
UART0	UART0_TXD_PC11	A1	BT_UART0_TXD	
	UART0_RXD_PC10	B1	BT_UART0_RXD	
	UART0_CTS_PC12	A2	BT_UART0_CTS	
	UART0_RTS_PC13	B2	BT_UART0_RTS	
I2S	I2S_DO_PB04	M2	BT_REG_EN	
	I2S_DI_PB03	M1	HOST_BT_RST	
	I2S_BCLK_PB01	L2	BT_WAKE_HOST	
	I2S_LRCLK_PB02	L3	BT_PWR_EN	
	I2S_MCLK_PB00	L1	HOST_WAKE_BT	
MSC	MSC0_D0_SSI0_DR_PA23	E11	MSC0_D0	TP7
	MSC0_D1_SSI0_DT_PA22	F12	MSC0_D1	TP9
	MSC0_D2_SSI0_CE1_PA21	F11	MSC0_D2	TP8
	MSC0_D3_SSI0_GPC_PA20	G12	MSC0_D3	TP10
	MSC0_CMD_SSI0_CE0_PA25	D12	MSC0_CMD	TP11
	MSC0_CLK_SSI0_CLK_PA24	E12	MSC0_CLK	TP12

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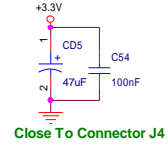
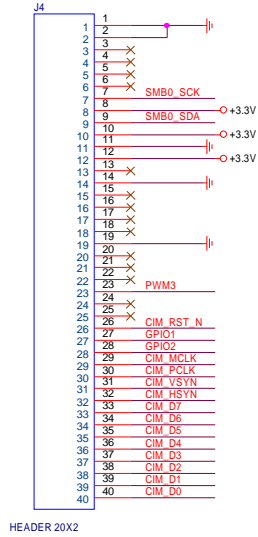


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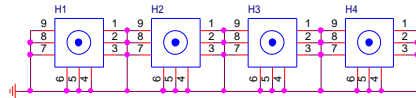
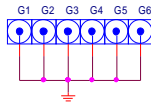
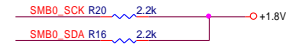
CAMERA



X1500_8X8_BGA109_V1.2



PWM3 >>> 5



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Data	Revision	Change
July 14 2016	Rev1.0	1. First Revision
Sep. 8 2016	Rev1.1	<ol style="list-style-type: none"> 1. Exchange the "OTG_DM" and the "OTG_DP" on the page 5 2. Add test points TP13, TP14, TP15 and TP16 on the page 5 3. Add R48, R49, R55 and R56 resistors and select UART2 to debug on the page 5 4. Add R20 and R16 pull-up resistors on the page 7 5. Exchange the "SMB0_SCK" and the "SMB0_SDA" at the CPU side on the page 7
Sep. 8 2016	Rev1.2	<ol style="list-style-type: none"> 1. Change the "VDDIO", "VDDIO_5T", "VL", "VCCIO", "SFC_VCC" and "BT_VDDIO" to +1.8V power supply on the page 3, 5, 6 2. Change the I/O signals' pull-up power supplies to +1.8V power supply on each paper 3. Add R61 0Ohm resistor on the page 3 4. Add R57, R58 and Q5 components to the "GREEN LED" circuit on the page 4 5. Delete R26 and D2 components from the "RESET LED" circuit on the page 4 6. Change U5's model from GD25Q127CS1G to GD25LQ128CSIG 7. Update the "POWER ARCHITECTURE" and "SYSTEM ARCHITECTURE" on the page 1,2 9. Add U12, and C85 components to the "BLUETOOTH" circuit on the page 6
Mar. 7 2017	Rev1.2.1	<ol style="list-style-type: none"> 1. Change the model of U19 to RT9013-18PB 2. Change R34 to NC

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