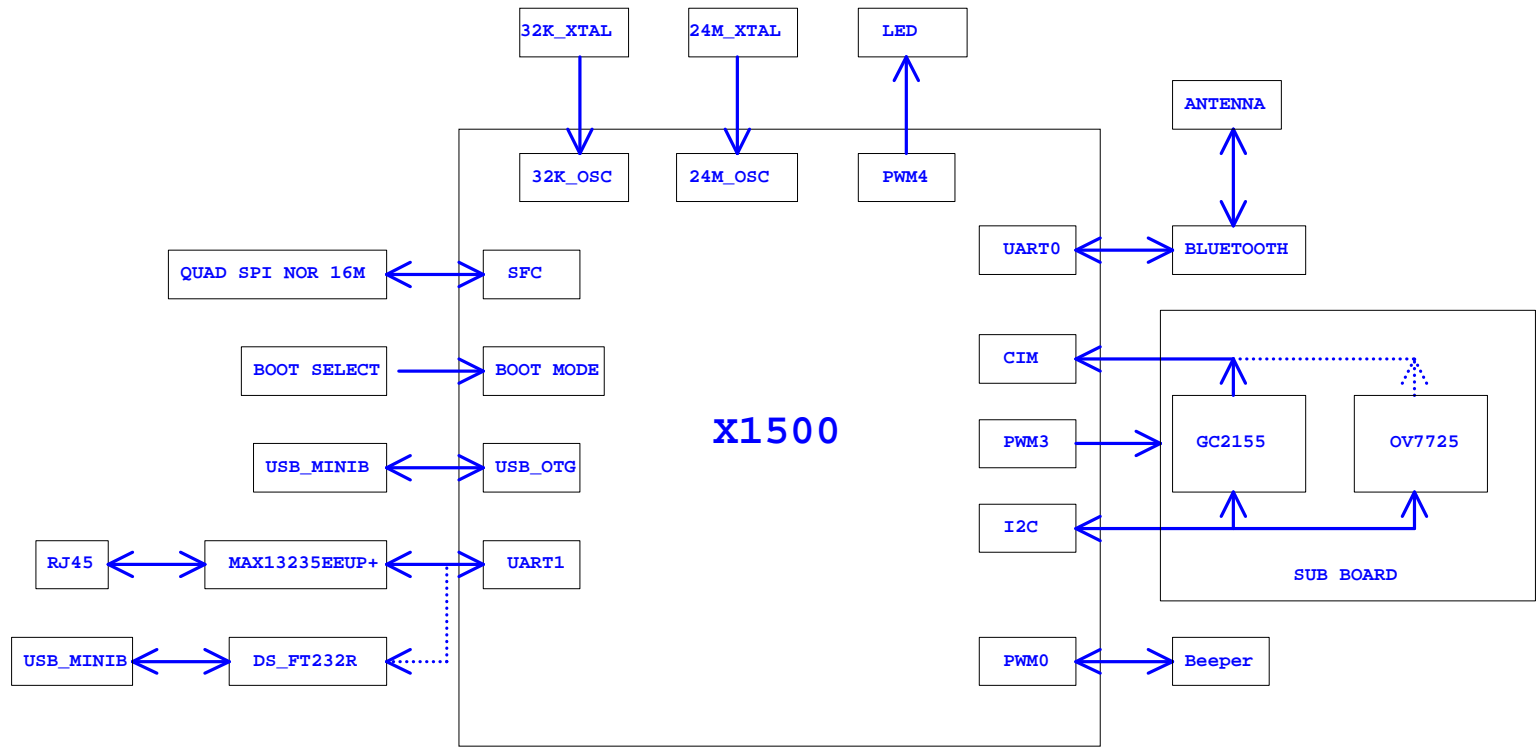


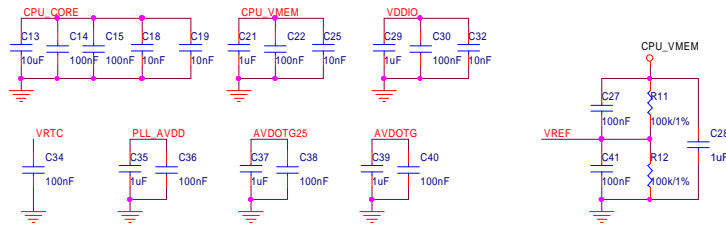
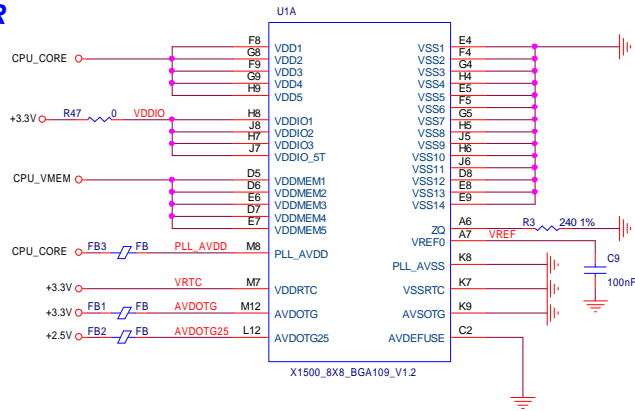
## RD\_X1500\_YAK\_V1.1.1

COVER SHEET/POWER ARCHITECTURE	1
SYSTEM ARCHITECTURE	2
CPU_PWR/POWER SUPPLY	3
BOOT/TRIGGER/BEEPER	4
HOST INTERFACE	5
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HISTORY	8

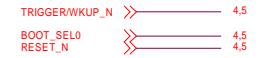
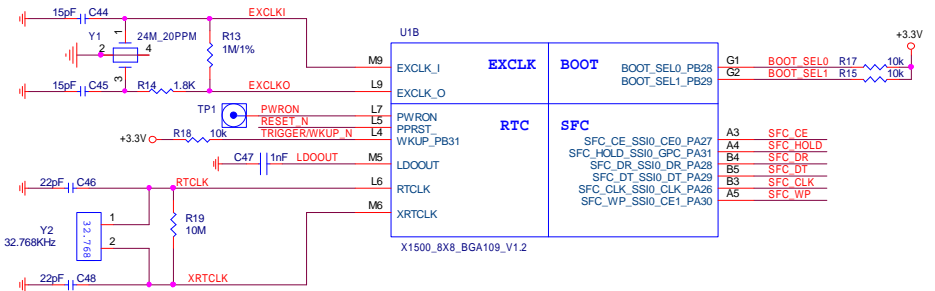


INGENIC SEMICONDUCTOR CO.,LTD			
RD_X1500_YAK			
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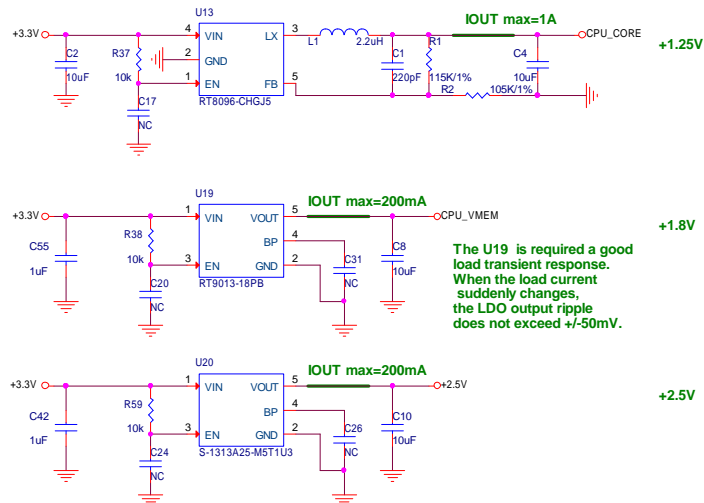
## CPU\_POWER



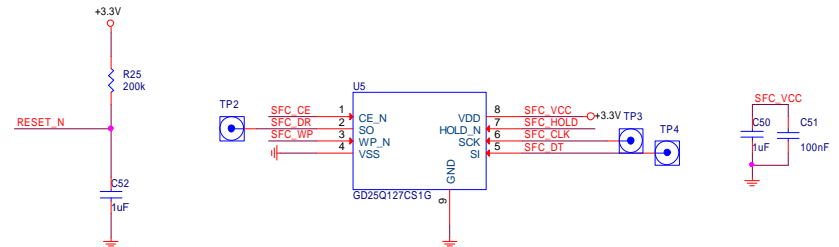
## BOOT & CLOCK



## MIAN POWER

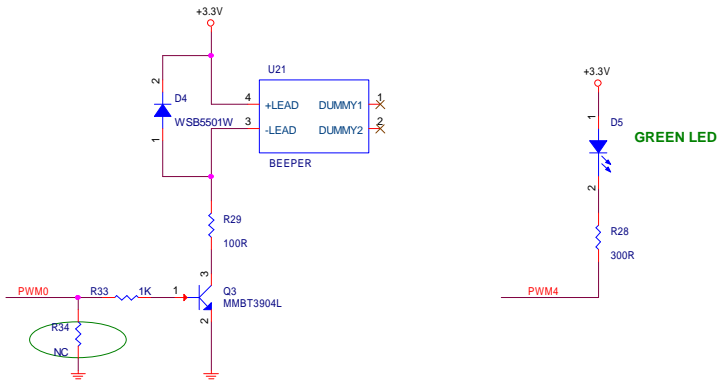


## RC\_RESET & SFC



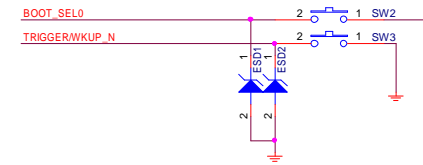
INGENIC SEMICONDUCTOR CO.,LTD			
Title RD_X1500_YAK			
Size A3	Document Number CPU_PWR/POWER SUPPLY	Rev 1.1.1	
Date: Wednesday, March 08, 2017	Sheet 3	of 8	

## BEEPER



R34 should be added to the BEEPER circuit when PWM0 is not connected to PC25

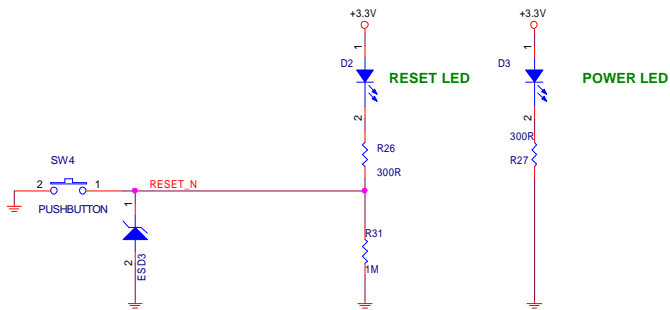
## BOOT CFG & TRIGGER



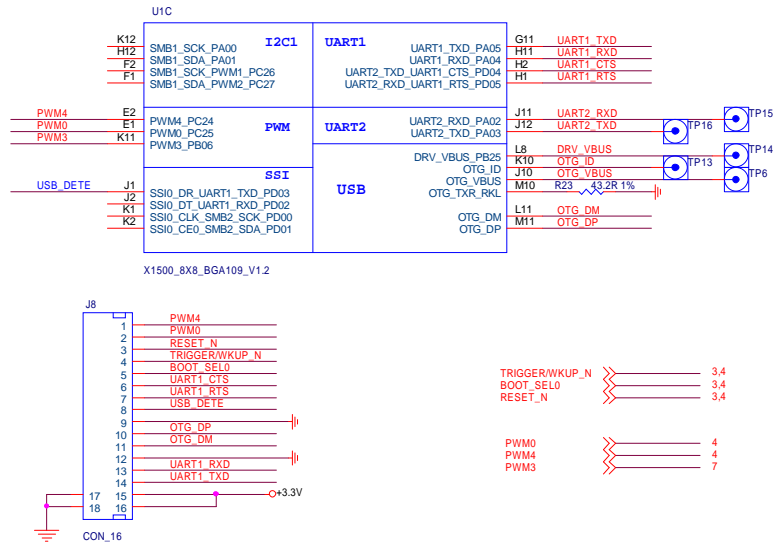
### Boot Mode Select

BOOT_SEL0	BOOT FROM
HIGH	SFC Boot @ 24M(default)
LOW	USB Boot @ 24M

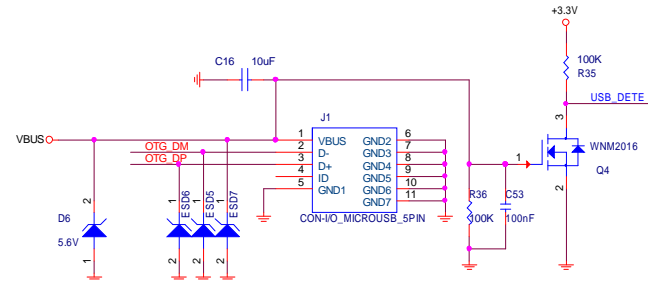
## RESET & INDICATE



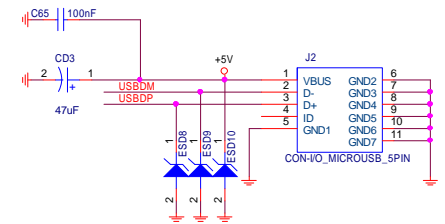
INGENIC SEMICONDUCTOR CO.,LTD			
Title RD_X1500_YAK			
Size A3	Document Number BOOT/TRIGGER/BEEPER	Rev 1.1.1	
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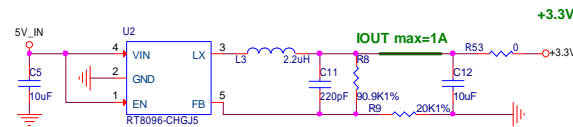
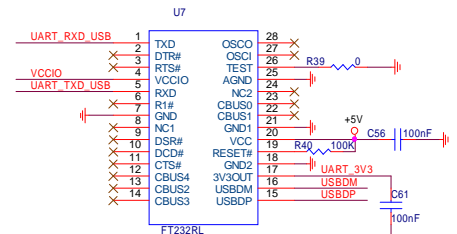
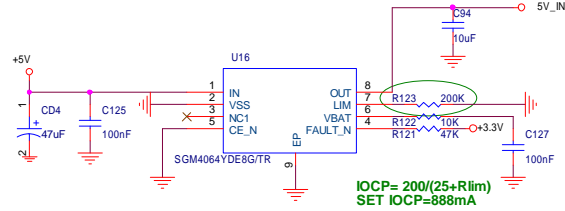
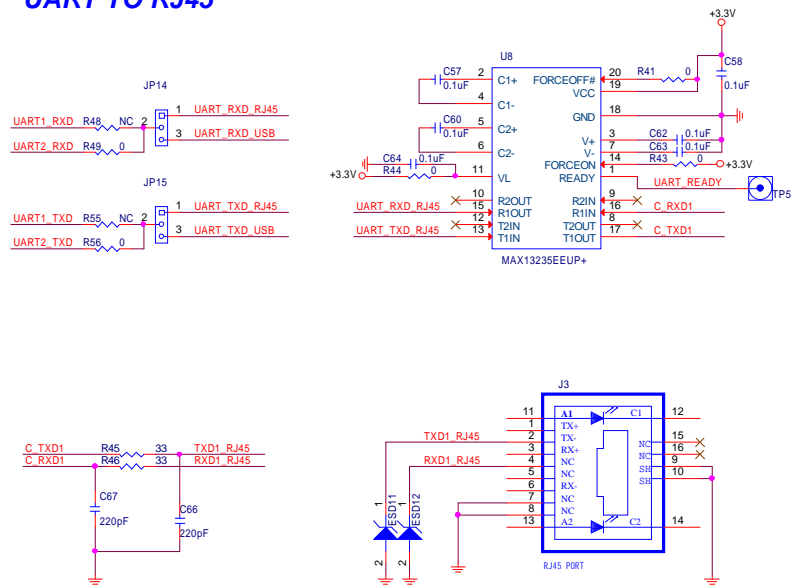
### USB



### UART TO USB & POWER SUPPLY



### UART TO RJ45

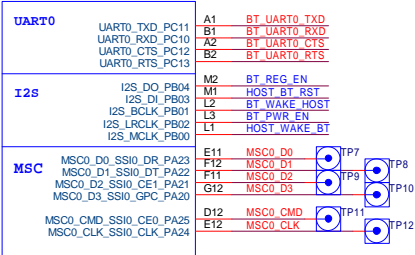


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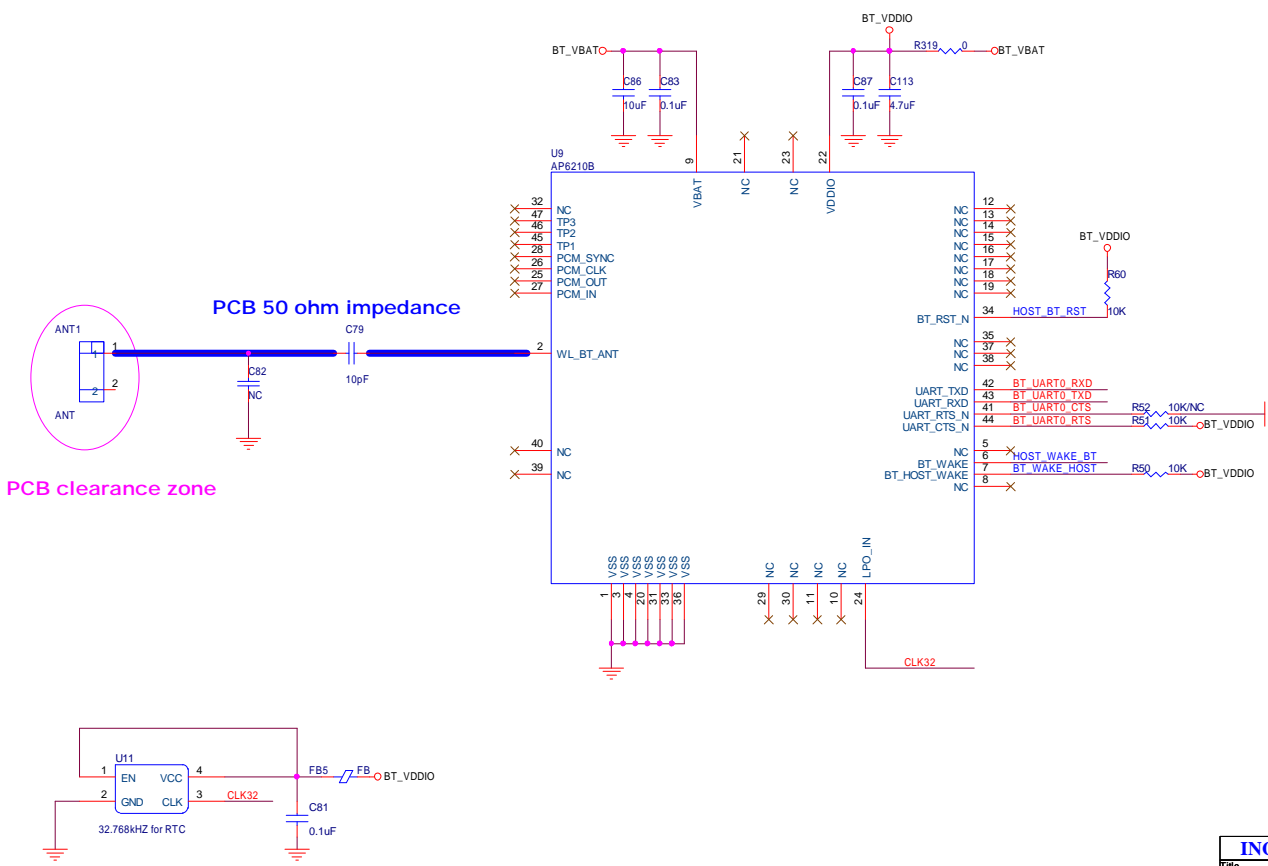
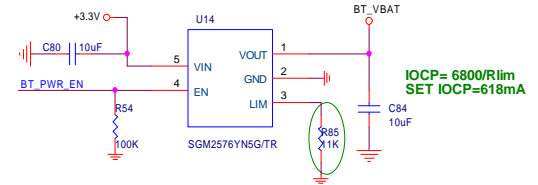
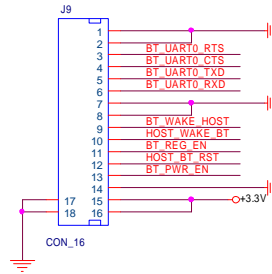
Title			RD_X1500_YAK
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Document Number			HOST_INTERFACE
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# BLUETOOTH

UID



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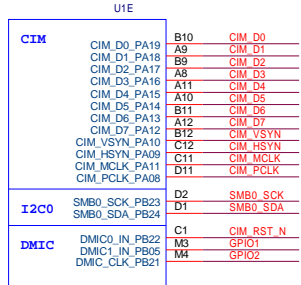


PCB 50 ohm impedance

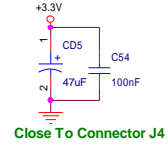
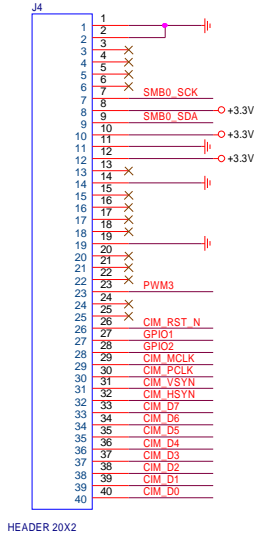
PCB clearance zone

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Title RD_X1500_YAK			
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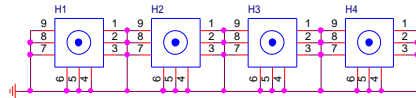
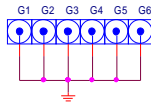
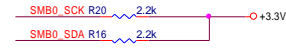
# CAMERA



X1500\_8X8\_BGA109\_V1.2



PWM3 >> 5



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Data	Revision	Change
July 14 2016	Rev1.0	1. First Revision
Sep. 8 2016	Rev1.1	1. Exchange the "OTG_DM" and the "OTG_DP" on the page 5 2. Add test points TP13, TP14, TP15 and TP16 on the page 5 3. Add R48, R49, R55 and R56 resistors and select UART2 to debug on the page 5 4. Add R20 and R16 pull-up resistors on the page 7 5. Exchange the "SMB0_SCK" and the "SMB0_SDA" at the CPU side on the page 7
Mar. 7 2017	Rev1.1.1	1. Change the model of U19 to RT9013-18PB 2. Change R34 to NC

<b>INGENIC SEMICONDUCTOR CO.,LTD</b>			
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